GSFC PREFERRED PARTS LIST PPL-17

SEPTEMBER 1984

(NASA-TM-88769) GSFC PREFERRED PARTS LISTS PPL-17 (NASA) 94 p HC A05/MF A01 CSCL 13B

N86-28264

Unclas G3/31 43396

GODDARD SPACE FLIGHT CENTER

This document was prepared by the Parts Branch of the Goddard Space Flight Center and the Preferred Parts Mission of the Sperry Corp.

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PREFACE

PURPOSE

This document contains a listing of preferred parts, part upgrading procedures, part derating guidelines, and part screening procedures to be used in the selection, procurement, and application of parts for GSFC space systems and ground support equipment.

AUTHORITY

The GSFC PPL is authorized and invoked by Goddard Management Instructions (GMI) 5330.6, Implementation of the Goddard Space Flight Center Parts Program.

STANDARDIZATION

MIL-STD-975, the NASA Standard (EEE) Parts List (NSPL), is the prime reference document for preferred electronic parts for NASA. The GSFC Preferred Parts List (PPL-17), complements MIL-STD-975 by listing additional part types and part categories not included in MIL-STD-975. Parts or styles listed in MIL-STD-975 are identified in PPL-17 as a convenience to users. Several part types listed in MIL-STD-975 are not identified in PPL-17. They are considered to be nonstandard, and are so noted in the PPL. Where conflicts exist between the NSPL and PPL-17, PPL-17 takes precedence.

All parts not specifically identified in the current issues of MIL-STD-975 or the GSFC PPL or which are not procured to the specification given in MIL-STD 975 or PPL are non-standard. These parts shall be used only, with the approval of the GSFC Project Office, if needs cannot be satisfied with a standard part.

QUALITY LEVELS

Consistent with MIL-STD-975, PPL-17 specifies two levels of quality. Grade 1 parts are higher quality, government-specification-controlled parts intended for critical applications. Grade 2 parts are high quality government-specification-controlled parts for use in applications where grade 1 parts are not required.

The parts listed in this document meet the requirements of a Military or NASA specification. When a PPL listed part is purchased, the specification listed for the part and the recommended manufacturer(s) or the manufacturers on the QPL for the part must be referenced in the procurement request.

All specifications listed in the PPL are maintained on file in the Parts Branch for reference purposes. GSFC personnel can obtain copies of specifications through their division offices from the Parts Branch Library, code 310.1, telephone (301) 344-7240. Contractors, approved domestic and foreign experimenters, and international cooperative project working groups can obtain copies

of the PPL and copies of referenced documents, except MIL specifications, by a written request via the cognizant project office. All others may obtain copies of the PPL through the National Technical Information Service (NTIS), Springfield, VA 22161 or the GIDEP data bank. Requests for Military Specifications should be directed to:

Commanding Officer Naval Publications and Forms Center, Code 3015 5801 Tabor Avenue Philadelphia, PA 19120

REVISIONS

The PPL will be reissued during 1986. Portions may be changed and updated prior to that date, as required. Parts not now listed, for which a substantial or critical usage is anticipated, should be brought to the attention of the Parts Branch so that those parts may be considered as candidates for evaluation and possible future listing in MIL-STD-975 or the GSFC PPL. Call (301) 344-8923 or (301) 344-6485.

PART CHARACTERISTICS

Electrical characteristics are specified at 25°C ambient, unless otherwise noted.

CRITERIA FOR LISTING PARTS

Parts are listed in the PPL based on the following criteria:

- (1) they can be procured to a high reliability military or NASA specification;
- (2) they have complied with an approved series of qualifying criteria;
- (3) they are judged by the GSFC Parts Branch to be available and not redundant to other parts in the GSFC PPL or MIL-STD-975.

USER RESPONSIBILITY

MIL-STD-975 and the PPL serve the Center covering both Flight and Ground Support Equipment applications and needs. It is the responsibility of the user, the product assurance engineer, and flight assurance manager to insure that the proper grade level parts are selected from MIL-STD-975 and the PPL commensurate with the criticality of the application.

PARTS APPLICATIONS

MIL-HDBK-978, NASA Parts Application Handbook, is intended to maintain a parts technology baseline for NASA centers and NASA contractors and to maximize standard parts usage. It is an integral part of the NASA standard parts program.

Those part categories covered in MIL-HDBK-978 that are also found in PPL-17 are: Microcircuits (Microelectronic Devices), Transistors, Diodes, Capacitors, Resistors, Connectors, Filters, Protective Devices, Relays, Transformers and Inductors. Some other features found in the handbook are: Cost Factors, Definitions, Construction Details, Operating Characteristics, Failure Mechanisms, Screening Techniques, Environmental Considerations, Selection Criteria, Circuit Application, Failure Rates and Radiation Effects.

PARTS UPGRADING

For some types of parts listed in MIL-STD-975 and the PPL, Grade 1 parts are not listed. Appendix A gives guidelines for upgrading a Grade 2 part for use in a Grade 1 application. In all cases, upgrading must be approved by submission of a non-standard part approval rquest. This additional testing does not provide a part that is equivalent to the Grade 1 part. Subsequent testing never can duplicate design and processing controls that are imposed during manufacturing.

PARTS DERATING

Conservative application stresses are an important design tool for decreasing part degradation, improving failure rates, and prolonging the useful life of parts. For guidance, recommended part derating factors are tabulated in Appendix B.

PARTS SCREENING

Screening is designed to eliminate quality defects that will prevent a part from meeting its intended performance requirements. Screening is not a substitute for the design and processing controls that can be applied to a part during manufacturing to improve its reliability. Appendix C gives screening guidelines that should be used when a nonstandard part must be procured because no standard part is available.

PARTS RADIATION EFFECTS

Space radiation can present a hazard to electronic parts on space missions. Appendix D gives information on radiation effects on electronic parts.

REFERENCED SPECIFICATIONS

Unless noted otherwise, all specifications referenced in the PPL are the issue in effect on the date of PPL issue.

PARTS INFORMATION DIRECTORY

Assistance in the selection of parts, parts specifications, manufacturers surveys, incoming inspection, screening evaluation tests and failure analysis services for all parts are available from the Parts Branch of the Product Assurance Division.

For assistance on electronic parts problems and questions in direct support of specific projects, users should contact the cognizant parts specialist assigned to the respective project. If unknown, the identity can be determined by contacting the project office.

For general evaluation information of electronic parts, part specifications, and part qualifications, users may contact a specialist in the particular part category, as listed below:

PART CATEGORY	SPECIALIST ,	TELEPHONE (301) 344-
Capacitors	P. Jones	5910
Connectors	J. Lawrence	5640
Crystals	V. Patel	6382
Diodes	M. Robertson	5910
Electro Optics	L. Hilliard	5987
Filters	V. Patel	6382
Fuses	J. Henegar	5345
Inductors	F. Kreis	7339
Microcircuits	S. Bryant	7437
PC Boards	H. Chernikoff	5984
) F. Kreis	7339
Relays	J. Lawrence	5640
Resistors	F. Kreis	7339
Thermistors	J. Henegar	5345
Transformers	F. Kreis	7339
Transistors	M. Robertson	5910
Wire and Cable	J. Lawrence	5640
All	Preferred Parts	6588
		6220

Additional services in support of the GSFC parts program are:

FUNCTION	CONTACT	<u>TELEPHONE</u> (301) 344-
Electronic Parts Qualification Testing, Maintenance of the PPL	B. Baldini	8923
Electronic Parts Incoming Test, Inspection, and Screening	W. Owens	6134
Data Systems		7635
Failure Analysis Destructive Physical Analysis	B. Baldini	8923

FUNCTION	CONTACT	TELEPHONE
		(301) 344-
Dackaging Process Specialist	H. Chernikoff	5984
Packaging Process Specialist) F. Kreis	7339
Padiation Effects	J. Adolphsen	8896
Radiation Effects	D. Cleveland	7437

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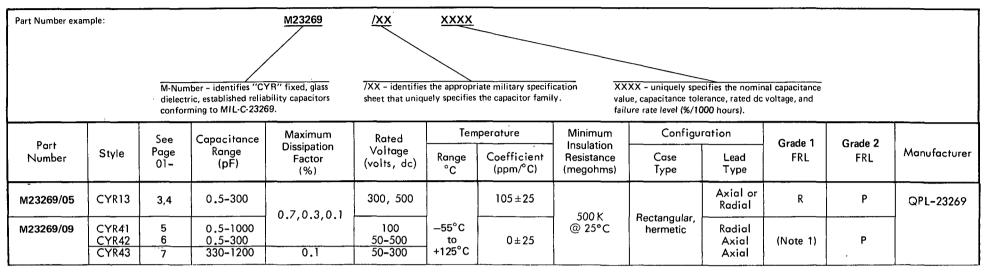
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Index of Preferred Capacitors

Style	Description	Specification	. Refer To
CCR	Ceramic, Temperature-compensating, Fixed	MIL-C-20	MIL-STD-975
CDR	Ceramic, Chip, Multiple-layered, Fixed Styles CDR01, 03, 04, 05, 06	MIL-C-55681	MIL-STD-975
CKR ²	Ceramic, Fixed	MIL-C-39014	MIL-STD-975
CLR ^{3, 4}	Tantalum (non-solid) electrolytic, Fixed	MIL-C-39006	MIL-STD-975
CRH	Plastic (metalized), Fixed	MIL-C-83421	MIL-STD-975
CSR ^{5, 6}	Tantalum (solid) electrolytic, Fixed	MIL-C-39003	MIL-STD-975
CWR	Tantalum Chip, Fixed	MIL-C-55365	MIL-STD-975
CYR	Glass, Fixed Styles CYR10, 15, 20, 30	MIL-C-23269	MIL-STD-975
L	Styles CYR13, 41, 42, 43, 51, 52, 53		Pages 01-2 to 01-7

- 1. CKR styles are to be limited to maximum capacitance values as follows: CKR05— 33,000 pf CKR11— 4,700 pf CKR14— 47,000 pf CKR06—333,000 pf CKR12— 10,000 pf CKR15— 180,000 pf CKR styles shall be purchased to revision C of MIL-C-39014.
- 2. CLR styles with ratings above 100 volts are not to be used for Grade I applications.
- 3. A non-standard parts approval is needed if the requirements of notes 1 and 2 are to be waived.
- 4. The CLR79 style wet slug tantalum capacitors shall be subjected to an acid indicator leak test in accordance with paragraphs III and IV of GSFC screening procedure SP01.23.
- 5. EFFECTIVE SERIES RESISTANCE SEEN BY CSR STYLE CAPACITORS SHOULD BE EQUAL TO OR GREATER THAN ONE OHM/VOLT. AS NOTED IN MIL-STD-975, THE CSR STYLE OF CAPACITOR IS NOT RECOMMENDED FOR USE IN APPLICATIONS OF LESS THAN ONE OHM/VOLT, AS IN POWER SUPPLY FILTERS.
- 6. Surge current testing shall be performed on CSR style capacitors for all Grade 1 applications per MIL-STD-975.

MIL-C-23269, CAPACITORS Fixed, Glass Dielectric, Established Reliability



NOTES:

01-2 PPL 17 September, 1984

^{1.} No Grade 1 parts are available at the present time.

M23269/05, STYLE CYR13 Fixed, Glass Dielectric, Established Reliability

Сар	Capacitance		Rated	Part Numbe	Part Number M23269/05 -	
Value (pF)	Tolerance (±)	Factor (%)	Voltage (volts, dc)	Grade 1 FRL = R(0.01)	Grade 2 FRL = P(0.1)	
0.5	0.25pF			5001	4001	
1.0	0.25pF			5002	4002	
1.5	0.25pF		ļ	5003	4003	
2.2	0.25pF			5004	4004	
	0.50pF			5005	4005	
2.7	0.25pF			5006	4006	
3.0	0.25 pF			5007	4007	
	0.50pF	0.7		5008	4008	
3.3	0.25pF			5009	4009	
3.6	0.25pF			5010	4010	
	0.50pF 0.25pF			5011	4011	
3.9	0.25 pF			5012	4012	
4.3	0.25pF			5013	4013	
	0.50pF			5014	4014	
4.7	0.25pF			5015	4015	
5.1	0.25 pF		500	5016	4016	
5.6	0.25 pF		300	5017	4017	
	5%			5018	4018	
6.2	0.25pF			5019	4019	
	5%			5020	4020	
6.8	0.25pF			5021	4021	
	_ 5%			5022	4022	
7.5	0.25 pF		1	5023	4023	
	5%	0.3		5024	4024	
8.2	0.25 pF			5025	4025	
L	5%			5026	4026	
9.1	0.25 pF			5027	4027	
	5%			5028	4028	
10	0.25pF			5029	4029	
	5%			5030	4030	
11	0°.25 pF			5031	4031	
	5%		l	5032	4032	
				l		

Cap	acitance	D:	D	Part Number	M23269/05-
Value (pF)	Tolerance (±)	Dissipation Factor (%)	Rated Voltage (volts, dc)	Grade 1 FRL = R(0.01)	Grade 2 FRL = P(0.1)
12	0.25 pF 5% 2%			5033 5034	4033 4034
13	5%			5035 5036	4035 4036
15	2% 5%			5037 5038	4037 4038
16	2% 5%			5039 5040	4039 4040
18	2% 5%	0.3		5041 5042	4041 4042
20	2% 5%			5043 5044	4043 4044
22	2% 5%			5045 5046	4045 4046
24	2% 5%		500	5047 5048	4047 4048
27	1% 2% 5%		300	5049 5050 5051	4049 4050 4051
30	1% 2% 5%			5052 5053 5054	4052 4053 4054
33	1% 2% 5%	0.1		5055 5056 5057	4055 4056 4057
36	1% 2% 5%			5058 5059 5060	4058 4059 4060
39	1% 2% 5%		i	5061 5062 5063	4061 4062 4063
43	1%			5064	4064

M23269/05, STYLE CYR13 (continued) Fixed, Glass Dielectric, Established Reliability

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Capacitance		Dissipation	Rated	Part Number	M23269/05-
Value (pF)	Tolerance (± %)	Factor (%)	Voltage (volts, dc)	Grade 1 FRL = R(0.01)	' Grade 2 FRL = P(0.1)
43	2 5			5065 5066	4065 4066
47	1 2 5			5067 5068 5069	4067 4068 4069
51	1 2 5			5070 5071	4070 4071
56	1 2 5			5072 5073 5074	4072 4073 4074
62	1 2			5075 5076 5077	4075 4076 4077
.	5 1 2	0.1	500	5078 5079 5080	4078 4079 4080
	5	· · ·		5081 5082	4081 4082
75 	2 5			5083 5084 5085	4083 4084 4085
82 	2 5			5086 5087 5088	4086 4087 4088
91	2 5			5089 5090	4089 4090
100	1 2 5			5091 5092 5093	4091 4092 4093
110	1 2			5094 5095	4094 4095

Сар	acitance	Dissipation	Rated	Part Number	M23269/05-
Value (pF)	Tolerance (± %)	Factor (%)	Voltage (volts, dc)	Grade 1 FRL = R(0.01)	Grade 2 FRL = P(0.1)
110	5			5096	4096
	1			5097	4097
120	5			5098	4098
<u> </u>				5099 5100	4099 4100
130	ا ہٰ ا			5100 5101	4100
100	2 5			5102	4101
	i			5103	4103
150	2			5104	4104
	5		500	5105	4105
	1			5106	4106
160	2			5107	4107
	5			5108	4108
]			5109	4109
180	. 2			5110	4110
ļ	5	0.1		5111	4111
000				5112	4112
200	2 5			5113	4113
	3			5114 5115	4114 4115
220	'			5116	4116
220	2 5			5117	4117
 	1			5118	4118
240	2			5119	4119
] -~~	2 5			5120	4120
l	1 1	1	300	5121	4121
270	2			5122	4122
	2 5			5123	4123
	1			5124	4124
300	2 5		i	5125	4125
	5			5126	4126

M23269/09, STYLE CYR41 Fixed, Glass Dielectric, Established Reliability

Capacitance		Dissipation	Rated	Part Number M23269/09-		
Value (pF)	Tolerance (±)	Factor (%)	Voltage (volts, dc)	Grade 2 FRL = P(0.1)		
0.5 1.5 2.7 3.3 3.9 4.7	0.25 pF 0.25 pF 0.25 pF 0.25 PF 0.25 pF 0.25 pF	0.5		4001 4002 4003 4004 4005 4006		
5.6	0.25 pf 0.25 pf 5% 0.25 pf			4007 4008 4009		
8.2	5% 0.25pF			4010 4011 4012		
12	5% 0.25 pF 5%			4013 4014 4015		
15	0.25 pF 2% 5%	0.3		4016 4017 4018		
18	0.25pF 2% 5%		100	4019 4020 4021		
22	0.25 pF 2% 5%			2% 5%	100	4022 4023 4024
27	1% 2% 5%			4025 4026 4027		
33	1% 2% 5%			4028 4029 4030		
39	1% 2% 5%			4031 4032 4033		
47	1% 2% 5%	0.1		4034 4035 4036		
56	1% 2% 5%			4037 4038 4039		
68	1% 2% 5%			4040 4041 4042		

Capacitance		Dissipation	Rated	Part Number M23269/09-
Value (pF)	Tolerance (± %)	Factor Voltage Grade		Grade 2 FRL = P(0.1)
82	1 2 5			4043 4044 4045
100	1 2 5			4046 4047 4048
120	1 2 5			4049 4050 4051
150	1 2 5			4052 4053 4054
180	1 2 5			4055 4056 4057
220	1 2 5			4058 4059 4060
270	1 2 5	0.1	100	4061 4062 4063
330	1 2 5)	100	4064 4065 4066
390	l 2 5			4067 4068 4069
470	1 2 5			4070 4071 4072
560	1 2 5			4073 4074 4075
680	1 2 5			4076 4077 4078
820	1 2 5			4079 4080 4081
1000	1 2 5		<u> </u>	4082 4083 4084

M23269/09, STYLE CYR42 Fixed, Glass Dielectric, Established Reliability

Can	acitance			Part Number M23269/09-
Сар	de l'une	Dissipation	Rated	Fall Nombel Wi23209/09-
Value (pF)	Tolerance (±)	Factor (%)	Voltage (volts, dc)	Grade 2 FRL = P(0.1)
0.5 1.5 2.7 3.3 3.9 4.7	0.25 pF 0.25 pF 0.25 pF 0.25 pF 0.25 pF 0.25 pF	0.7		4101 4102 4103 4104 4105 4106
4.7 5.6 6.8	0.25 pF 0.25 pF 5% 0.25 pF			4107 4108 4109
8.2	5% 0.25 pF			4110 4111 4112
12	5% 0.25 pF 5% 0.25 pF			4113 4114 4115
15	2% 5%	0.3	500	4116 4117 4118
18	0.25 pF 2% 5%			4119 4120 4121
22	0.25 pF 2% 5%			4122 4123 4124
27	1% 2% 5%	1.0		4125 4126 4127
33	1% 2 % 5%			4128 4129 4130
39	1% 2% 5%			4131 4132 4133

Сар	acitance	Dissipation	Rated	Part Number M23269/09 -
Value (pF)	Tolerance (± %)	Factor (%)	Voltage (volts, dc)	Grade 2 FRL = P(0.1)
47	1 2 5		500	4134 4135 4136
56	1 2 5		300	4137 4138 4139
68	1 2 5		300	4140 4141 4142
82	1 2 5		300	4143 4144 4145
100	1 2 5			4146 4147 4148
120	l 2 5	0.1		4149 4150 4151
150	1 2 5		100	4152 4153 4154
180	1 2 5			4155 4156 4157
220	2 5			4158 4159 4160
270	1 2 5		50	4161 4162 4163
300	1 2 5		30	4164 4165 4166

M23269/09, STYLE CYR431 Fixed, Glass Dielectric, Established Reliability

Сар	acitance	Rated	Part Number M23269/09-
Value (pF)	Tolerance (± %)	Voltage (volts, dc)	Grade 2 FRL = P(0.1)
	1		4301
330	2		4302
	5	300	4303
- 130	1	300	4304
390	2		4305
	5		4306
	1		4307
470	2		4308
	5		4309
	1		4310
560	2	100	4311
	5		4312
	1		4313
680	2		4314
	5		4315

Сар	acitance	Rated	Part Number M23269/09-
Value (pF)	Tolerance (± %)	Voltage (volts, dc)	Grade 2 FRL = P(0.1)
	1		4316
820	2		4317
	5		4318
	1		4319
1000	2	50	4320
	5		4321
	1		4322
1200	2		4323
	5		4324

NOTE: 1. Dissipation factor = 0.1%

Index of Preferred Connectors¹

Style	Description	Specification	Refer To
G311P10	Power Connectors, Solder Contacts (sub-miniature)	GSFC S-311-P-10	Page 02-2
311P409	Power Connectors, Crimp Removable Contacts (sub-miniature)	GSFC S-311-P-4/9	Page 02-3
311P407	Power connectors, Crimp Removable Contacts (sub-miniature High Density)	GSFC S311P-4/7	Page 02-4
NLS	High Density, Miniature	MSFC 40 M38277	MIL-STD-975
NB	Miniature (200°C)	MSFC 40 M39569	MIL-STD-975
NBS	Electrical, Miniature, Circular (200°C)	MSFC 40 M38298	MIL-STD-975

NOTES:
1. OTHER PARTS ARE LISTED IN MIL-STD-975, BUT GSFC CONSIDERS ONLY THE ONES LISTED ABOVE AS STANDARD PARTS.

POWER CONNECTORS Rack and Panel, Sub-Miniature, Solder Contacts

· -	Cor	ntacts	For the Wish	Grade	1 & Grade 2		
Construction	Qty.	Туре	For Use With Wire Size	GSFC Type ¹	Specification GSFC	Manufacturer	Remarks
Receptacle, Rectangular Plug, Rectangular	9 15 25 37 50 9 15 25 37	Socket Socket Socket Socket Pin Pin Pin		G311P10B-1S-C-15 G311P10B-2S-C-15 G311P10B-3S-C-15 G311P10B-4S-C-15 G311P10B-5S-C-15 G311P10-1P-C-15 G311P10-2P-C-15 G311P10-3P-C-15	S-311-P-10	ITT Cannon Electric TRW Cinch Connectors	All GSFC type connectors: "-15" in type indicates 0.154 inch (0.39 mm) dia. mounting hole, 0.120 inch (0.31 mm) dia. is available; indicated by "-12."
	50	Pin		G311P10-5P-C-15			

^{1.} C = 20 gamma residual magnetism level; other levels B = 200 and D = 2 gamma are available.

POWER CONNECTORS Rack and Panel, Sub-Miniature, Crimp Removable Contacts

	Cor	ntacts	For		Grade	1 & Gra	ide 2		
Construction			Use With	Sł	nell		Contact		Remarks
	Qty.	Туре	Wire Size	GSFC Type ¹ 311P409	GSFC Specification	GSFC Type	GSFC Specification	Manufacturer	
Receptacle, Rectangular	9 15 25 37 50	Socket	AWG #	-1S-B-15 -2S-B-15 -3S-B-15 -4S-B-15 -5S-B-15	S 211 B 4/0	G10S1	S 211 D 4/10	AMP, Inc.	All GSFC type connectors: "-15" type indicates 0.154 inch (0.39 mm) dia. mounting hole, 0.120 inch (0.31 mm) dia. is available; indicated by "-12."
Plug, Rectangular	9 15 25 37 50	Pin	22- 24	-1P-B-15 -2P-B-15 -3P-B-15 -4P-B-15 -5P-B-15	S-311-P-4/9	G10P1	S-311-P-4/10	TRW Cinch Connectors	

NOTES:
1. 8 = 200 gamma residual magnetism level. Other levels are available; if required, consult the parts specialist.

POWER CONNECTORS Rack and Panel, Sub-Miniature, High Density, Crimp Removable Contacts

	Cor	ntacts	For		Grade	1 & Gra	de 2		
Construction			Use With	Sł	nell		Contact		Remarks
	Qty.	Туре	Wire Size	GSFC Type ⁻¹ 311P407	Specification GSFC	GSFC Type	Specification GSFC	Manufacturer	
Receptacle, Rectangular	15 26 44 62 78 104	Socket	AWG # 22- 24-	-1S-B-15 -2S-B-15 -3S-B-15 -4S-B-15 -5S-B-15 -6S-B-15	S-311-P-4/7	G08\$1	C 211 D 4/0	Amo Inc	All GSFC type connectors: "-15" type indicates 0.154 inch (0.39 mm) dia. mounting hole, 0.120 inch (0.31 mm) dia. is available; indicated by "-12."
Plug, Rectangular	15 26 44 62 78 104	Pin	24- 26- 28	-1P-B-15 -2P-B-15 -3P-B-15 -4P-B-15 -5P-B-15 -6P-B-15	S-311-P-4//	G08P1	S-311-P-4/8	Amp, Inc.	

^{1.} B = 200 gamma residual magnetism level. No other residual magnetism levels are available for this type of connector.

Index of Preferred Filters 1, 2, 3

Style	Description	Specification	Refer To
FS11	Electromagnetic Interference Suppression	MIL-F-28861/1	MIL-STD-975
FS50	Electromagnetic Interference Suppression	MIL-F-28861/5	MIL-STD-975

- 1. MIL-STD-975 LISTS THE MIL-F-18327 BAND PASS FILTER. GSFC CONSIDERS ONLY THE ONES LISTED ABOVE AS STANDARD PARTS.
- Presently, there are no Grade 1 filters. Non-standard part approval and up-grading are required for intended use of the Grade 2 devices in a Grade 1 program. See Appendix A for recommended up-grading procedures.
- 3. a. THE TORQUE USED IN MOUNTING THESE FILTERS IS CRITICAL. EXCESSIVE TORQUE CAN DAMAGE THE INTERNAL CAPACITOR. USE THE MINIMUM TORQUE NECESSARY FOR THE MECHANICAL CONNECTION TO CREATE A GOOD ELECTRICAL CONNECTION TO GROUND. IN NO CASE SHOULD THE TORQUE EXCEED THE LIMIT GIVEN IN THE DETAIL SPECIFICATION. FOR MORE INFORMATION, CONSULT THE PART'S SPECIALIST.
 - b. THE FILTERS MUST BE TREATED AS BEING HEAT SENSITIVE. HEAT SINK THE DEVICE WHEN SOLDERING TO THE FILTER.

Index Of Preferred Fuses

Style	Description	Specification	Refer To
FM04A	Fuse, Subminiature	MIL-F-23419	Page 04-2
FM08A	Fuse, Subminiature	MIL-F-23419	Page 04-2

FUSE Subminiature⁷ (Axial Leads)

	Maxi-	Maximum Short			Grade 1 ^{1,4}					Grade 2 ²		
Current Rating ^{3,6} (Amperes)		Circuit Interrupt Current @ Rated VDC (Amperes)	Voltage Drop @ Rated Current (Min–Max) (Volts)	Maxi- mum Cold Resis- tance (ohms)	Mil Part Number	Specification	Manu- facturer	Voltage Drop @ Rated Current (Min–Max) (Volts)	Maxi- mum Cold Resis- tance (ohms)	Mil Part Number	Specification	Manu- facturer
1/8			.85-1.15	2.31	FM08A 125V 1/8A	1		.85-1.15	2.70	FM04A 125V 1/8A	1	
1/4			.590800	.781	FM08A 125V 1/4A			.544736	.960	FM04A 125V 1/4A		
3/8			.527713	.462	FM08A 125V 3/8A	Note 6		.527713	.560	FM04A 125V 3/8A	Note 6	
1/2			.488660	.308	FM08A 125V 1/2A	1		.510690	.365	FM04A 125V 1/2A		
3/4			.145197	.187	FM08A125V 3/4A			.134182	.215	FM04A125V 3/4A		
1			.157213	.138	FM08A125V 1A	MIL-F-23419/8	QPL-23419	.157213	.165	FM04A125V 1A	MIL-F-23419/4	QPL-23419
1-1/2			.153207	.088	FM08A125V 1-1/2A	WII L+F-234 19/6	QFL-25417	.153207	.105	FM04A125V 1-1/2A	WIL-F-25417/4	QFL-20417
2	125		.144196	.0605	FM08A125V 2A			.144196	.072	FM04A125V 2A		
2-1/2		300	.125169	.0462	FM08A125V 2-1/2A			-	-	Note 5		
3			.139187	.0388	FM08A125V 3A			.128173	.047	FM04A125V 3A		
4			.110150	.0253	FM08A125V 4A			.110150	.029	FM04A125V 4A		
5			.087118	.0154	FM08A125V 5A			.087118	.019	FM04A125V 5A		
7			.087118	.0110	FM08A125V 7A			,		Note 5		
10			.073099	.0066	FM08A125V 10A							
15.	32		.065087	.0044	FM08A32V 15A							

- 1. GSFC requires additional screening for Grade 1 applications per Appendix C, Table 04.
- 2. GSFC requires additional screening for Grade 2 applications per Appendix C, Table 04.
- 3. Refer to Appendix B, Table 04 for Fuse Derating outline for all applications.
- 4. GSFC recommends the use of redundant circuits for critical flight applications.
- 5. No Grade 2 part exists at the present time. Use the listed Grade 1 part.

- 6. THE FLIGHT USE OF FUSES RATED ½ AMPERE AND LESS REQUIRES APPLICATION APPROVAL BY THE APPLICABLE GSFC PROJECT OFFICE. EVIDENCE OF ACTUAL CURRENT LEVELS (INCLUDING STEADY-STATE, REPETITIVE PULSES AND TRANSIENTS) MUST BE SUBMITTED WITH THE APPROVAL REQUEST.
- Subminiature fuses are not mechanically rugged and are susceptible to handling and assembly damage. Use special handling and soldering for these heat sensitive parts.

Index of Preferred Inductors

Style	Description	Specification	Refer To
MIL-T-27/146	Audio Frequency, High Q	MIL-T-27	MIL-STD-975
MS21367 ¹	Coil, Fixed, Radio Frequency, Micro Miniature, Shielded, Iron Core	MIL-C-15305	MIL-STD-975
MS21368 ¹	Coil, Fixed, Radio Frequency, Micro Miniature, Shielded, Iron Core	MIL-C-15305	MIL-STD-975
MS21369 ¹	Coil, Fixed, Radio Frequency, Micro Miniature, Shielded, Iron Core	MIL-C-15305	MIL-STD-975
MS90538 ¹	Coil, Fixed, Radio Frequency, Subminiature, Iron Core	MIL-C-15305	MIL-STD-975
MS90539 ¹	Coil, Fixed, Radio Frequency, Subminiature, Iron Core	MIL-C-15305	MIL-STD-975
MIL-C-39010/01	Coil, Fixed, Radio Frequency, Micro Miniature, Shielded, Phenolic Core	MIL-C-39010	MIL-STD-975
MIL-C-39010/02	Coil, Fixed, Radio Frequency, Micro Miniature, Shielded, Iron Core	MIL-C-39010	MIL-STD-975
MIL-C-39010/03	Coil, Fixed, Radio Frequency, Micro Miniature, Shielded, Ferrite Core	MIL-C-39010	MIL-STD-975
MIL-C-39010/06	Coil, Fixed, Radio Frequency, Micro Miniature, Phenolic Core	MIL-C-39010	MIL-STD-975
MIL-C-39010/07	Coil, Fixed, Radio Frequency, Micro Miniature, Powdered Iron Core	MIL-C-39010	MIL-STD-975

NOTES:

1. MIL-C-15305 PARTS ARE NOT SCREENED AND ARE CONSIDERED TO BE NON-STANDARD PARTS. FOR SPACE FLIGHT USE THEY MUST BE SCREENED AS OUTLINED IN APPENDIX C, TABLE 05.

Index of Preferred Relays (Grade 1 and Grade 2)

Style	Description	Specification	Refer To
P2/33	Latching	GSFC S311 P2(06)/33	Page 06-3
P2/37	Latching	GSFC S311 P2(06)/37	Page 06-3
P2/39	Nonlatching	GSFC S311 P2(06)/39	Page 06-2
P2/42	Nonlatching	GSFC S311 P2(06)/42	Page 06-2
P2/47	Nonlatching	GSFC S311 P2(06)/47	Page 06-2
P2/48	Nonlatching	GSFC S311 P2(06)/48	Page 06-2
P2/50	Latching	GSFC S311 P2(06)/50	Page 06-3
P2(06)/19	Nonlatching	GSFC S311 P2(06)/19	Page 06-2
P2(06)/23	Nonlatching	GSFC S311 P2(06)/23	Page 06-2
P2(06)/27	Latching	GSFC S311 P2(06)/27	Page 06-3
P2(06)/35	Latching	GSFC S311 P2(06)/35	Page 06-3
M39016/6	Nonlatching	MIL-R-39016/6	MIL-STD-975 ^{2, 3}
M39016/9	Nonlatching	MIL-R-39016/9	Page 06-2 ^{1, 3}
M39016/11	Nonlatching	MIL-R-39016/11	MIL-STD-975 ^{2, 3}
M39016/12	Latching	MIL-R-39016/12	Page 06-3 ^{1, 3}
M39016/13	Nonlatching	MIL-R-39016/13	Page 06-2 ^{1, 3}
M39016/14	Nonlatching	MIL-R-39016/14	Page 06-2 ^{1, 3}
M39016/15	Nonlatching	MIL-R-39016/15	Page 06-2 ^{1, 3}
M39016/20	Nonlatching	MIL-R-39016/20	MIL-STD-975 ^{2, 3}
M39016/21	Nonlatching	MIL-R-39016/21	MIL-STD-975 ^{2, 3}
M39016/29	Latching	MIL-R-39016/29	Page 06-3 ^{1, 3}
M39016/30	Latching .	MIL-R-39016/30 .	MIL-STD-975 ^{2, 3}
M39016/31	Latching	MIL-R-39016/31	Page 06-3 ^{1, 3}
M39016/38	Nonlatching	MIL-R-39016/38	MIL-STD-975 ^{2, 3}
MS27400	Nonlatching	MIL-R-6106	Page 06-2 ^{1, 3}
MS27401	Nonlatching	MIL-R-6106	Page 06-2 ^{1, 3}
MS27742	Latching	MIL-R-6106	Page 06-3 ^{1, 3}

- These military styles are limited to Grade 2 applications. For Grade 1 applications, use equivalent GSFC part numbers (refer to pages 06-2 and 06-3).
- These styles listed in MIL-STD-975 are limited to Grade 2 applications. No equivalent Grade 1 parts are currently available.
- 3. FOR ALL GRADE 2 PARTS LISTED HERE OR IN MIL-STD-975, THE FOLLOWING SHALL APPLY:
 - A. THE PURCHASE ORDER SHALL SPECIFY THAT THE PARTS SHALL BE SUPPLIED WITH UNPAINTED ENCLOSURES, AND NO CADMIUM OR ZINC PLATING (INTERNAL OR EXTERNAL) SHALL BE USED.
 - B. A DPA SHALL BE PERFORMED PER GSFC S-311-70 (REFER TO APPENDIX A, PAGE A-1 FOR SAMPLING PLAN).

Relays, Nonlatching

	Electric	al Data		Med	chanical D	ata		Grade 1	· · · · · · · · · · · · · · · · · · ·		Grade 2 ⁶		
Contact Rating at 28 vdc Resistive ² (amps)	Coil V Nominal (vdc)	Pick-up (max) (vdc)	Nominal dc Coil Resistance (ohms)	Contact Form ³	Package Type	Terminal Type	GSFC Part Number ¹	GSFC Specification S-311-P-2(06)	Mfr.	MIL Part Number ¹	Specification	Mfr.	Remarks
1.04	26.5 18.0 12.0 9.0 6.0	18.0 13.5 9.0 6.8 4.5	1560 880 390 220 98	2 Form C (2PDT)	TO-5 Can	Wire Leads	P2/39-01 P2/39-02 P2/39-03 P2/39-04 P2/39-05	/39	Teledyne	M39016/9-062P M39016/9-061P M39016/9-080P M39016/9-059P M39016/9-058P	MIL-R-39016/9		
1.04	26.5 18.0 12.0 9.0 6.0	18.0 13.5 9.0 6.8 4.5	1560 880 390 220 98	2 Form C (2PDT)	TO-5 Can	Wire Leads	P-2/48-01 P-2/48-02 P-2/48-03 P-2/48-04 P-2/48-05	/48	Teledyne	M39016/15-081P M39016/15-080P M39016/15-079P M39016/15-078P M39016/15-077P	MIL-R-39016/15	QPL-39016	Coil Transient Suppression
1.04	26.5 12.0 6.0	13.5 5.4 2.7	720 115 28	4 Form C (4PDT)	Low ⁵ Profile	Pins	P-2/42-03 P-2/42-02 P-2/42-01	/42	Genicom	M39016/14-002M M39016/14-007M M39016/14-005M	MIL-R-39016/14		
2.04	26.5 12.0 6.0	13.5 5.4 2.7	1350 210 56	2 Form C (2PDT)	1/2 Crystal Can	Solder Lugs	P-2/47-01 P-2/47-02 P-2/47-03	/47	Genicom	M39016/13-060P M39016/13-065P M39016/13-064P	MIL-R-39016/13		
10.0	28.0	18.0	320	2 Form C		Solder Lugs	P-2(06)/23-01	/23	Leach	MS27401-13			
				(2PDT)	Can	Pins	P-2(06)/23-02	,		MS27401-14	MIL-R-6106	QPL-6106	
10.0	28.0	18.0	290	4 Form C	One Inch	Solder Lugs	P-2(06)/19-01	/19	Leach	MS27400-9	IVIIL-N-0100	QFL-0106	
				(4PDT)	Cube	Pins	P-2(06)/19-02			MS27400-10			

- GSFC part number is for 1.500 inch min. lead/length, whereas the corresponding MIL part number is for 0.500 inch min. lead length.
- 2. For contact rating for other types of loads (inductive, capacitive, lamp, motor), contact parts specialist.
- 3. Refer to NARM Engineers' Relay Handbook for definition of forms (example: form C = single pole, double throw, break before make).
- 4. Contacts also suitable for low level applications.
- 5. 15.5 mm x 15.5 mm x 8.1 mm high (.610" x .610" x .320").
- 6. For Grade 2 parts, see requirements on page 06-1.

Relays, Latching

	Electric	al Data		Med	hanical D	ata		Grade 1			Grade 2 ⁷		
Contact Rating at 28 vdc Resistive ² (amps)	Coil V Nominal (vdc)	Pick-up (max) (vdc)	Nominal dc Coil Resistànce (ohms)	Contact Form ³	Package Type	Terminal Type	GSFC Part Number ¹	GSFC Specification S-311-P-2(06)	Mfr.	MIL Part Number ¹	Specification	Mfr.	Remarks
1.04	26.5 18.0 12.0 9.0 6.0	18.0 13.5 9.0 6.8 4.5	2000 1130 500 280 120	2 Form C (2PDT)	TO-5 Can	Wire Leads	P2/33-01 P2/33-02 P2/33-03 P2/33-04 P2/33-05	/33	Teledyne	M39016/12-060P M39016/12-050P M39016/12-058P M39016/12-057P M39016/12-056P	MIL-R-39016/12		
1.04	26.5 18.0 12.0 9.0 6.0	18.0 13.5 9.0 6.8 4.5	2000 1130 500 280 120	2 Form C (2PDT)	TO-5 Can	Wire Leads	P-2/37-01 P-2/37-02 P-2/37-03 P-2/37-04 P-2/37-05	/37	Teledyne	M39016/29-060P M39016/29-059P M39016/29-058P M39016/29-057P M39016/29-056P	MIL-R-39016/29	QPL-39016	Coil Transient Suppression
	24.0 12.0	18.0 6.8	1000 250			Solder Hook	P2/50-01 P2/50-02		Potter				
2.04	24.0 12.0 24.0 12.0	18.0 6.8 18.0 6.8	1000 250 1000 250	2 Form C (2 PDT)	1/2 Crystal Can	Pins	P2/50-03 P2/50-04 P2/50-05 P2/50-06	/50	and Brumfield (AMF)	Note 6			
2.04	26.5	13.5	975	4 Form C (4PDT)	Low ⁵ Profile	Pins	P-2(06)/27-01	/27	Genicom	P-2(06)/27-01	MIL-R-39016/31		
25.0	28.0	18.0	450	3 Form C	One Inch	Solder Lugs	P-2(06)/35-01	/35	Leach	MS27742-1	MIL-R-6106	QPL-6106	
			<u> </u>	(3PDT)	Cube	Pins	P-2(06)/35-02	<u> </u>		MS27742-2			

- 1. See Note 1 on Page 06-2.
- 2. See Note 2 on Page 06-2.
- 3. See Note 3 on Page 06-2.
- 4. See Note 4 on Page 06-2.
- 5. See Note 5 on Page 06-2.
- 6. Use Grade 1 parts.
- 7. FOR GRADE 2 PARTS, SEE REQUIREMENTS ON PAGE 06-1.

Index of Preferred Resistors

Style	Description	Specification	Refer To
RBR	Wire wound, Accurate	MIL-R-39005	MIL-STD-975
RWR	Wire wound, Power	MIL-R-39007	MIL-STD-975
RCR ¹	Composition	MIL-R-39008	MIL-STD-975
RER	Wire wound, Power, Chassis Mounted Non-Inductive and Inductive winding	MIL-R-39009	MIL-STD-975
RLR	Film, General Purpose	MIL-R-39017	MIL-STD-975
RTR	Wire wound, Variable	MIL-R-39015	MIL-STD-975
RJR	Non-wire wound, variable	MIL-R-39035	MIL-STD-975
RN(X) ²	Film, High Stability	MIL-R-55182	MIL-STD-975
RZO ³	Fixed Film Networks	MIL-R-83401	MIL-STD-975

- 1. GSFC considers RCR styles at the "S" failure rate suitable for both Grade 1 and Grade 2 applications.
- GSFC does not consider type "C" terminal material to be readily weldable, and recommends using type "N" in welding applications. Type "C" and "R" may be used in soldering applications. Styles 75 and 90 are available only with type "C" terminal material.
- GSFC considers RZO styles listed in MIL-STD-975 acceptable for use in Grade 2 applications. For Grade 1 applications, consult the Parts Specialist.

Index of Preferred Diodes^{1, 3}

C	0 - 4 00		
Grade 11	Grade 22	·	
Type	Type	İ	.
Designation	Designation	Description	Refer To
JANS	JANTXV	1	
Туре	No.		
	IN645-1		
	IN647-1	Small Signal	
	IN649-1		MIL-STD-975
IN754A-1	IN746A-1		
thru	thru	Zener Voltage Regulator	
IN759A-1	IN759A-1		
	IN821-1		
l .	IN823-1		
	IN825-1	Voltage Reference	
	IN827-1	1	1
ļ	IN829-1		MIL-STD-975
	IN935B		
	IN937B		
	thru	Zener Voltage Reference	
	IN940B		
	IN941B		
ļ	IN943B	Voltage Reference	Page 08-3
	IN944B		
	IN945B		
	IN962B		
]	thru		
	IN992B	Zener Voltage Regulator	
IN962B-1	IN962B-1	Zone: Venage negalities	
thru	thru		MIL-STD-975
IN973B-1	IN973B-1		
	IN1202A	High Power	
	IN2970B		
i	thru	Zener Voltage Regulator	
	IN3051B		
.	IN3595	Contachina	2 22 5
l	IN3600	Switching	Page 08-2
	IN3821A		
	thru	Voltage Regulator	
	IN3828A		MIL-STD-975
	IN3891	Fast Switching	
	IN3893	Power Rectifier	
}	IN4099		
I	thru	Voltage Regulator	Page 08-4
l	IN4135	1	
L			

	T		1	
Grade 11 Type	Grade 22 Type			
Designation	Designation	Description	Refer To	
JANS	JANTXV	Description	Melei 10	
Туре				
IN4148-1	IN4148-1	Small Signal	-	
	IN4150-1	Switching	MIL-STD-975	
j	IN4153-1	·		
1	IN4245			
	IN4247 IN4249	Power	Page 08-7	
	IN4306 IN4307	Switching	Page 08-2	
	IN4370A-1			
	thru	Voltage Regulator	MIL-STD-975	
}	IN4372A-1			
	IN4454-1	Switching	Page 08-2	
	IN4460			
	thru	Zener Voltage Regulator	MIL-STD-975	
	IN4496			
	IN4531	Switching	Page 08-2	
	IN4565A			
	thru	Voltage Reference		
	IN4569A			
	IN4570A			
	thru	Zener Voltage Reference	MIL-STD-975	
	IN4574A			
	IN4614			
l	thru	Voltage Regulator		
	IN4627			
	IN4942			
	IN4944	Fast Switching		
	IN4946	Power Rectifier	Page 08-6	
	IN4947			
	IN4948	ļ. <u>.</u>		
	IN4954	1		
	thru	Voltage Regulator	MIL-STD-975	
<u> </u>	IN4995	<u> </u>		
			-	

- Men no JANS diode is listed on the QPL, a Grade 2 diode may be upgraded for use in Grade 1 applications in accordance with Appendix A. A non-standard part approval is required.
- 2. JANTXV diodes must be subjected to the screening verification tests of Appendix E.
- 3. Refer to Appendix D for information on radiation effects.

Index of Preferred Diodes^{1, 3} (continued)

	7	· · · · · · · · · · · · · · · · · · ·	
Grade 11	Grade 22		
Туре	Туре		-
Designation	Designation	Description	Refer To
JANS	JANTXV	+	
Туре	No.		
	IN5139A	Voltage Variable	
	thru	Capacitor	Page 08-7
	IN5148A	Capacitor	•
	IN5285	•	
	thru	Current Regulator	
	IN5314		
	IN5415	F 0 :- 1:	
	thru	Fast Switching	
	IN5420	Power Rectifier	
	IN5550		
	thru	Power Rectifier	
	IN5554		
	IN5611	Voltage Suppressor	
	IN5614	Power Rectifier .	
	IN5615	Fast Switching	MIL-STD-975
		Power Rectifier	
·	IN5616	Power Rectifier	
	IN5617	Fast Switching	
	ļ	Power Rectifier	
	IN5618	Power Rectifier	
	IN5619	Fast Switching	
		Power Rectifier	
}	IN5620	Power Rectifier	
	IN5621	Fast Switching	
	1 :	Power Rectifier	
	IN5622	Power Rectifier	
1	IN5623	Fast Switching	
		Power Rectifier	
	IN5629A		
	thru	Zener Voltage	
	IN5665A	Suppressor	i
	1		

Grade 11	Grade 22		
Type Designation JANS	Type Designation JANTXV	Description	Refer To
Туре	No.		
	IN5711 IN5712	Schottky Barrier Switching	
ł	IN5768 IN5770 IN5772 IN5774	Array	MIL-STD-975
	IN5804 IN5806 IN5809 IN5811	High Power	
	IN5814 IN5816	Power Rectifier	
	IN5907	Zener Voltage Suppressor	
,	IN6073 thru IN6081	Fast Switching Power Rectifier	Page 08-6
	IN6100	Array	MIL-STD-975
·	IN6108	Allay	Page 08-8
	IN6102A thru IN6173A	Transient Voltage Suppressor	MIL-STD-975
	IN6320 thru IN6336	Voltage Regulator	Page 08-5
	2N2323A 2N2324A 2N2326A 2N2328A	SCR	MIL-STD-975

DIODES Switching, Silicon¹

Grade 1 ² Type Designation JANS	Grade 2 ³ Type Designation JANTXV	Specification MIL-S-19500	Manufacturer	Maximum Forward Voltage (Vdc)	Forward @ Current (mAdc)	Maximum Reverse Current (μAdc)	Reverse @ Voltage (Vdc)	Reverse Recovery Time (t _{rr}) (nsec)	Capacitance (pF)	Case Dwg .	Remarks
	1N3595	/241		0.88	50	0.001	125	3000	8.0		
	1N3600	/231		0.86	50	0.10	50		2.5	Note 4	•
	1N4306	/278	QPL-19500	0.81	10	5.0	75	4	2.0	4 lead flat pack 5	Two matched discrete her- metically sealed diodes are encapsulated in a plastic module.
	1N4307	/284	QFL-19500	0.81	10	5.0	75		2.0	8 lead flat pack Note 6	Four matched discrete hermetically sealed diodes are encapsulated in a plastic module.
	1N4454-1	/144		1.0	10	0.1	50	4	2.0	DO35	
	1N4531	/116		1.0	10	5.0	75	5	4.0	Note 4	

- 1. See MIL-STD-975 for additional types.
- 2. See Note 1 on Page 08-1.
- 3. See Note 2 on Page 08-1.
- 4. This case does not meet the dimensional criteria for any JEDEC outline. See MIL-S-19500 detail specification for case outline dimensions.
- 5. 11.30 mm x 4.37 mm x 7.62 mm.
- 6. 11.30 mm x 4.37 mm x 12.45 mm.

DIODES Voltage Reference, Silicon¹

Grade 1 ² Type Designation JANS	Grade 2 ³ Type Designation JANTXY	Specification MIL-S-19500	Manufacturer	Reference Voltage (min/max) (Vdc)	Zener Current (mAdc)	Voltage Change (Vdc)	/ Temperature	Impedance (ohms)	Zener Current (mAdc)	Case Dwg.
	<u>1N944B</u> 1N945B	/157	QPL-19500	11.12/12.28	7.5	<u>0.024</u> 0.012	-55°C - 150°C	30	7.5	D07

NOTES:

See MIL-STD-975 for additional types.
 See Note 1 on Page 08-1.
 See Note 2 on Page 08-1.

DIODES (Page 1 of 2) Voltage Regulator, Silicon¹

											
Grade 12	Grade 2 ³						M. D:	V 10 T	Max.		
Type	Type	Specification	Manufacturer	Non Reference	ninal e Voltage	Max. Impedance	Max. Diss. $T_A = 25^{\circ}C$	Voltage Temp. Coefficient	Storage	Case	Remarks
Designation	Designation	MIL-S-19500		V _Z (V) @	IZ (mA)	Z _Z (Ohms)	(w)	(%/°C)	Temp. (℃)	Dwg.	
JANS	JANTXV ³										
	1N4099			6.8	56			+0.060			
	1N4100			7.5	51			+0.065			
	1N4101			8.2	46			+0.070		Ì	
	1N4102 1N4103			8.7 9.1	44 42			+0.075			
	1N4104	ļ]	10.0	38	200					
	1N4105			11.0	35			.0.000			
	1N4106			12.0	32			+0.080			
	1N4107			13.0	29						
	1N4108			14.0	27	<u> </u>					
	1N4109		:	15.0	25			+0.085			
	1N4110 1N4111	1		16.0 17.0	24 22	100					
	1N4111			18.0	21				+0.090		
	1N4113			19.0	20			+0.090			
	1N4114	j	ļ	20.0	19						
	1N4115			22.0	17	150		+0.030		5°C D014	Low Noise Devices
	1N4116	/435	QPL-19500	24.0	16	100	0.40		175°C		
	1N4117 1N4118	, ,00	di 2 10000	25.0	15		5 5		,,,,		
			ļ	27.0 28.0	14 14	<u> </u>		-			
	1N4119 1N4120			30.0	13						
	1N4120 1N4121			33.0	12	200					
	1N4122			36.0	11			+0.095			
	1N4123			39.0	9.8			,			
	1N4124			43.0	8.9	250					
	1N4125			47.0	8.1						
	1N4126 1N4127			51.0 56.0	7.5 6.7	300					10
	1N4127 1N4128			60.0	6.4	400					
	1N4129			62.0	6.1	500					
	1N4130			68.0	5.6			10.100			
	1N4131			75.0	5.1	700		+0.100	,		
	1N4132	1		82.0	4.6	800					
	1N4133		!	87.0	4.4	1000					
	1N4134 1N4135	}	1	91.0	4.2	1200					
	1114133	<u> </u>	L	100.0	3.8	1500					

^{1.} See MIL-STD-975 for additional types.
2. See Note 1 on Page 08-1.
3. See Note 2 on Page 08-1.

DIODES (Page 2 of 2) Voltage Regulator, Silicon¹

Grade 1 ² Type Designation JANS	Grade 2 ³ Type Designation JANTXV	Specification MIL-S-19500	Manufacturer	Referenc	Nominal Reference Voltage V _Z (V) @ I _Z (mA)		Max Diss. TL = 75°C (W) Note 5	Voltage Temp. Coefficient (%/°C)	Max. Storage Temp. (°C)	Case Dwg.	Remarks
	IN6320 IN6321 IN6322 IN6323 IN6324 IN6325 IN6326 IN6327 IN6328 IN6329 IN6330 IN6331 IN6332 IN6333 IN6334 IN6335 IN6336	/533	QPL-19500	6.8 7.5 8.2 9.1 10 11 12 13 15 16 18 20 22 24 27 30 33	20 20 20 20 20 20 20 9.5 8.5 7.8 7.0 6.2 5.6 5.2 4.6 4.2 3.8	3.0 4.0 5.0 6.0 6.0 7.0 7.0 8.0 10 12 14 18 20 24 27 32 40	0.500	.062 .068 .075 .076 .079 .082 .083 .079 .082 .083 .085 .086 .087	200	Note 4	Low Power

- See MIL-STD-975 for additional types.
 See Note 1 on Page 08-1.
 See Note 2 on Page 08-1.
 Microminiature, solid glass, non-cavity construction with dimensions 2.3mm ODX 5mm long.
 Lead temperature (T_L) at 3/8 inch from diode case.

DIODES Power Rectifiers, Fast Switching, Silicon¹

Grade 1 ² Type Designation JANS	Grade 2 ³ Type Designation JANTXV	Specification MIL-S-19500	Manufacturer	I _O (Adc)		Vam (wkg) [V (pk)]	Reverse Recovery Time (trr) (nsec)	Maximum Reverse Current [@] (μAdc)	Reverse Voltage (Vdc)	I _{FSM} (1/120 sec) (A pk)	Case Dwg.
	1N4942				@T _A = 55°C	200	150	1.0	200		DO15
	1N4944		QPL-19500	0.85		400	150		400	10	
	1N4946	/359				600	150		600		
	1N4947					800	250		800	15	
	1N4948					1000	500		1000	10	
,	1N6073				0.85 1.3 @T _A = 55°C 2.0	50		1.0	50	35 75 175	Note 4
	1N6074					100			100		
	1N6075					150			150		
	1N6076					50		5.0	50		
	1N6077	/503		1.3		100	30		100		
	1N6078					150			150		
	1N6079					50			50		
	1N6080			2.0		100		10.0	100		
	1N6081					150		}	150		

- 1. See MIL-STD-975 for additional types.
- See Note 1 on Page 08-1.
 See Note 2 on Page 08-1.
- 4. This case does not meet the dimensional criteria for any JEDEC outline. See MIL-S-19500 detail specification for case outline dimensions.

DIODES Power, Silicon¹

Grade 1 ²	Grade 2 ³	Specification MIL-S-19500	Manufacturer	Maximum Forward Voltage [V (pk)]	Forward Current [A (pk)]	Maxin	num Reverse C	Reverse		
Type Designation JANS	Type Designation JANTXV					25°C (μAdc) [@]	150°C (mAdc)	Reverse Voltage (Vdc)	Recovery Time t _{rr} (µsec)	Case Dwg.
	1 N4245 1 N4247 1 N4249	/286	QPL-19500	1.3	3.0	1.0	.15	200 600 1000	5	DO15

DIODES Voltage Variable Capacitor, Silicon

Grade 12	Grade 2 ³			Name in all Com	0 - 0 - 1		Min. Q		Max. Temp. (°C)	
Type Designation JANS	Type Designation JANTXV	Specification MIL-S-19500	Manufacturer	Nominal Cap. @V _R = 4Vdc (pF)	Cap. Ratio V _R = 4v to 60v (times)	Max. Cont. Work. Volts V _R (volts)	@f = 50MHz V _R = 4vdc	Max. Diss. (W) TA = 25°C		Case Dwg.
	1N5139A	/383	QPL-19500	6.8	2.7	60	350	0.4	175°C	
	1N5140A			10	3.2		300			j
	1N5141A			12						}
	1N5142A			15			250			
:	1N5143A			18						DO7
	1N5144A			22			200			507
	1N5145A			27						
	1N5146A			33						
	1N5147A			39						
	1N5148A			47						

NOTES: 1. See MIL-STD-975 for additional types.
2. See Note 1 on Page 08-1.
3. See Note 2 on Page 08-1.

DIODES Switching, Silicon¹, Arrays

Garde 1 ² Type Designation JANS	Grade 2 ³ Type Designation JANTXV	Specification MIL-S-19500	Manufacturer	Maximum Forward Voltage (Vdc)	Forward © Current (mAdc)	Maximum Reverse Current (ηAdc)	Reverse Voltage (Vdc)	Reverse Recovery Time (t _{rr}) (nsec)	Capacitance (pF)	Case Dwg.	Remarks
	1N6101	/517	QPL-19500	1.0	100	25	20	5	3	Note 4	Monolithic

- 1. See MIL-STD-975 for additional types.
 2. See Note 1 on Page 08-1.
 3. See Note 2 on Page 08-1.
 4. PLackage is 16-pin ceramic dual in-line package (DIP).

Index of Preferred Transistors 1, 3

Grade 11	Grade 2 ²		
Type Designa- tion JANS	Type Designa- tion JANTXV	Description	Refer To
	2N718A	Low Power-NPN	Page 09-2
2N918	2N918	RFNPN	MIL-STD-975
	2N1613	Medium Power-NPN	Page 09-3
İ	2N2060	Dual - NPN	
2N2219AL	2N2219A	Medium Power-NPN	1
2N2222A	2N2222A		
2N2369A	2N2369A	Lower Power—NPN	MIL-STD-975
	2N2432A 2N2484	Chopper-NPN Low Power-NPN	
	2N2605	Low Power-PNP	
	2N2857	RFNPN	
	2N2880	High Power-NPN	Page 09-4
2N2905AL	2N2905A	Medium Power-PNP	
2N2907A	2N2907A	Low Power - PNP	MIL-STD-975
	2N2920	Dual - NPN	
	2N2944A	Chopper—PNP	Page 09-3
	2N2945A	Chopper - PNP	MIL-STD-975
	2N2946A 2N3019	Chopper—PNP Medium Power—NPN	Page 09-3
	2N3251A	Low Power-PNP	
	2N3375	RF-NPN	
	2N3468 2N3501	Low Power—PNP Low Power—NPN	MIL-STD-975
]	2N3553	RFNPN	
	2N3637 2N3700 2N3716	Medium Power—PNP Low Power—NPN Low Power—NPN	
<u> </u>	2N3741	High Power-PNP	

	·		
Grade 11.	Grade 22		
Type	Туре		
Designa-	Designa-	Description	Refer To
tion	tion		
JANS	JANTXV		
1	2N3743	Low Power PNP	Page 09-3
	2N3749	High Power-NPN	
	2N3763	Medium Power-PNP	
	2N3765	Low Power PNP	
	2N3792	High Power-PNP	
	2N3810	Dual – PNP	MIL-STD-995
	2N3811	Dual - PNP	
	2N3821	J-FET(N-CH)	
	2N3822	J-FET (N-CH)	
	2N3823	J-FET (N-CH)	
	2N3866	RF-NPN	_
	2N3868	Medium Power-PNP	Page 09-2
]	2N3996	High Power-NPN	
1	2N4150	Medium Power-NPN	
	2N4399	High Power PNP	
	2N4416A	J-FET (N-CH)	
	2N4856	J-FET (N-CH)	MIL-STD-975
	2N4857	J-FET (N-CH)	
	2N4858	J-FET (N-CH)	
	2N4931	Medium Power-PNP	Page 09-2
	2N4957	RF-PNP	
]	2N5038	High Power-NPN	MIL-STD-975
	2N5114	J-FET (P-CH)	
	2N5115	J-FET (P-CH)	
<u> </u>	2N5116	J-FET (P-CH)	

Grade 11	Grade 22		
Туре	Type		
Designa-	Designa-	Description	Refer To
tion	tion		•
JANS	JANTXV		
Ì	2N5250	High Power-NPN	Page 09-4
}	2N5415S	Low Power-PNP	Page 09-2
i ·	2N5416	Low Power – PNP	MIL-STD-975
	2N5660	High Power-NPN	Page 09-4
	2N5662	Medium Power-NPN	Page 09-3
	2N5664	High Power-NPN	
	2N5665	High Power-NPN	
	2N5666	High Power-NPN	
	2N5667	High Power-NPN	MIL-STD-975
	2N5672	High Power-NPN	
1	2N5745	High Power—PNP	
1 .	2N6308	High Power-NPN	
}	2N6546	High Power-NPN	Page 09-4
	4N23		
	4N23A		MIL-STD-975
	4N24		
	4N24A	Photocoupler	
	4N47		
1	4N48		,
	4N49		

- When no JANS transistor is listed on the QPL, a Grade 2 transistor may be upgraded for use in Grade 1 applications in accordance with Appendix A. A non-standard part approval is required.
- 2. JANTXV transistors must be subjected to the screening verification of Appendix E.
- 3. Refer to Appendix D for information on radiation effects.

TRANSISTORS NPN, Silicon, Low Power¹

Grade 12	Grade 2 ³)		•		()		P+	
Type Designation JANS	Type Designation JANTXV	Specification MIL-S-19500	Manufacturer	hfE (min/max)	I _C (mAdc)	V _{CE} (Vdc)	I _{CBO} (nAdc) [©]	V _{CB} (Vdc)	V _{CE} (SAT) (Vdc)	I _C (mAdc)	I _B (mAdc)	BV _{CBO} (Vdc)	@T _A = 25°C (mW)	Case Dwg.
	2N718A	/181	QPL-19500	40/120	150	10	10	60	1.5	150	15	75	500	TO18

TRANSISTORS PNP, Silicon, Low Power¹

Grade 12	Grade 2 ³				(9					<u></u>		P	Switchi	ng Time	
Type Designation JANS	Type Designation JANTXV	Specification MIL-S-19500	Manufacturer	hff (min/max)	I _C (mAdc)	V _{CE} (Vdc)	I _{СВО} (nAdc) [©]	V _{CB} (Vdc)	V _{CE} (SAT) (Vdc)	I _C (mAdc)	I _B (mAdc)	BV _{CBO} (Vdc)	@T _A = 25°C (mW)	t _{on} (nsec)	t _{off} (nsec)	- Case Dwg.
	2N3251A	/323	OBL 10500	100/300	-10	-1	-20	-40	-0.25	-10	-1		360	70	250	TO18
	2N3765	/396	QPL-19500	40/140	-500	-1	-100	-30	-0.5	-500	-50	-60	500	43	115	TO46
	2N5415S	/485		30/120	-50	-10	500μΑ	200	-2.0	-50	-5	350	750	1000	140	T05

TRANSISTORS PNP, Silicon, Medium Power¹

						,	,									
Grade 12	Grade 2 ³				@				(5.4-7)	6	j		P _T	Switchi	ng Time	1
Type Designation JANS	Type Designation JANTXV	Specification MIL-S-19500	Manufacturer	hfE (min/max)	I _C (mAdc)	V _{CE} (Vdc)	I _{CBO} (nAdc) ⁽	V _{CE} (Vdc)	V _{CE} (SAT) (Vdc)	I _C (mAdc)	I _B (mAdc)	BV _{CBO} (Vdc)	@T _A = 25°C (mW)	t _{on} (nsec)	t _{off} (nsec)	Case Dwg.
	2N3763	/396		40/140	-500	-1	-100	-30						43	115	
	2N3868	/350	QPL-19500	30/150	-1500	-2	I _{CEX} = -1000	V _{CE} = -60Vdc	-0.5	-500	-50	-60	1,0	100	600	TO5
	2N4931	/397	1	50/200	-30	-10	-500	-200	-1.2	-30	-3	-250		not spe	ecified	ТО39
	2N3743			50/200	-30	-10	-500	-300	-1.2	-30	-3	-300		Not sp	ecified	T039

NOTES:

- 1. See MIL-STD-975 for additional types.
- 2. See Note 1 on Page 09-1.
- 3. See Note 2 on Page 09-1.

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TRANSISTORS NPN, Silicon, Medium Power¹

Grade 1 ²	Grade 2 ³				(ق				(ÿ		P±	Switchin	ng Time	
Type Designation JANS	Type Designation JANTXV	Specification MIL-S-19500	Manufacturer	h _{FE} (min/max)	I _C (mAdc)	V _{CE} (Vdc)	I _{CBO} (nAdc) [©]	V _{CB} (Vdc)	V _{CE} (SAT) (Vdc)	I _C (mAdc)	I _B (mAdc)	BV _{CBO} (Vdc)	@T _A = 25°C (mW)	t _{on} (nsec)	t _{off} (nsec)	Case Dwg.
	2N1613	/181		40/120	150	10	10	60	1.5	150	15	75		not sp	ncified	
	2N3019	/391	QPL-19500	100/300	150	10	ICES = 10nAdc	V _{CE} = 90Vdc	0.2	150	15	140	800	not spi	scilled	ТО5
	2N5662	/454		40/120	500	5	100	200	0.4	1000	100	250	1200	250	850	

TRANSISTORS PNP, Chopper, Low Power, Silicon¹

Grade 1 ²	Grade 2 ³				@	ı	r (on)	f = 1k I _E =		V _{EC} (ofs)	(Þ		P _T	
Type Designation JANS	Type Designation JANTXV	Specification MIL-S-19500	Manufacture	hFE (min)	I _C (mAdc)	V _{CE} (Vdc)	r _{ec} (on) (max) ((Ohms)	@ and I _B (mAdc)		(max) (Vdc)	l _E (mAdc)	I _B (mAdc)	(Vdc)	@T _A = 25°C (mW)	Case Dwg.
	2N2944A	/382	QPL-19500	100	1	-0.5	4	4	100	-0.6	0	1	-15	400	TO46
	2N2946A	/302	QPL-19500	50	-1	-0.5	8	- 1	100	-2.0	U	-1	-40	400	1046

- 1. See MIL-STD-975 for additional types.
 2. See Note 2 on Page 09-1.
 3. See Note 3 on Page 09-1.

TRANSISTORS NPN, Silicon, High Power^{1, 3}

Grade 1 ²	Grade 2 ³				(9				(<u>a</u>		P _T	
Type Designation JANS	Type Designation JANTXV	Specification MIL-S-19500	Manufacturer	hf E (min/max)	Ic (Adc)	V _{CE} (Vdc)	ICBO (mAdc)	_⊚ .Vcв (Vdc)	V _{CE} (SAT) (Vdc)	I _C (Adc)	I _B (Adc)	BVCBO (Vdc)	@T _C = 25°C (Watts)	Case Dwg.
	2N2880	/315		40/120	1	5	0.0004	80	0.25	1	0.1	110	30@ T _c = 125°C	Note 4
	2N5250	/380	QPL-19500	30/90	20	5	I _{CES} = 0.1 mAdc	V _{CE} = 125 Vdc	1.0	40	4	125	350	Note 4
	2N5660	/454		40/120	0.5	5	0.0001	200	0.4	1	0.1	250	20@ T _c = 125°C	TO66
	2N6546	/525		16/30	. 10	2	1	600	1.5	10	2	300	175	тоз

TRANSISTORS PNP, Silicon, High Power¹

Grade 1 ²	Grade 2 ³	9 15 1	<u> </u>		6)					@		. P _T	Switchi	ng Time	
Type Designation JANS	Type Designation JANTXV	Specification MIL-S-19500	Manufacturer	mer (min/max)	I _C (Adc)	V _{CE} (Vdc)	(mAdc)	(Vdc)	V _{CE} (SAT) (Vdc)	I _C (Adc)	I _B (Adc)	BV _{CBO} (Vdc)	@T _C = 25°C (Watts)	t _{on} (μ sec)	t _{off} (μ sec)	Case Dwg.
	2N3741	/441		30/100	-0.250	-1	-0.0001	-80	-0.6	-1	-0.125		25	0.4	1.0	ТО66
	2N3792	/379	QPL-19500	50/150	-1	-2	ICES = -1mAdc	V _{CE} = -70Vdc	-1	-5	-0.5	-80	150	1.5	2.0	тоз
	2N5745	/433		15/60	-10	-2	-1	-80	-1	-10	-1		200	1.0	3.0	

- 1. See MIL-STD-975 for additional types.
- 2. See Note 1 on Page 09-1.
- 3. See Note 2 on Page 09-1.
- 4. This case does not meet the dimensional criteria for any JEDEC outline. See MIL-S-19500 detail specification for case outline dimensions.

TRANSISTORS Field-Effect, N-Channel, Junction, Silicon¹,

Grade 1 ²	Grade 2 ³			V _{DG} and V _{DS}	VGS		V _{GS} (off)	@	9	IDSS	. (D	_	
Type Designation JANS	Type Designation JANTXV	Specification MIL-S-19500	Manufacturer	(max) (Vdc)	(max) (Vdc)	(mA)	max. (Vdc)	V _{DS} (Vdc)	I _D (nA)	(min/max) (mA)	V _{DS} (Vdc)	V _G s (Vdc)	P _T (mW)	Case Dwg.
	2N3821						-4			0.5/2.5		F	200	TO72
	2N3822	/375		50	-50	10]		2/10			300	10/2
	2N4857	/2.7	QPL-19500				-6	15	0.5	20/100	15	U	200	TO18
	2N4858	/385		40	-40	50	-4			8/80			360	1018

- 1. See MIL-STD-975 for additional types.
 2. See Note 1 on Page 09-1.
 3. See Note 2 on Page 09-1.



(Generic Part Numbers Shown)

Low Power Schottky,TTL	CMOS/Bulk
54LS00 54LS51 54LS139 54LS241 54LS395	4000A 4019A 4069UBR
54LS02 54LS54 54LS148 54LS244 54LS490	4002A 4020A 4070B
54LS03 54LS73 54LS151 54LS245 54LS670	4006A 4021A 4071B
54LS04 54LS74 54LS153 54LS251	4007A 4022A 4072B
54LS05	4008A 4023A 4073B
54LS08 54LS76 54LS157 54LS257	4009A 4024A, 4075B ^R
54LS10 54LS83A 54LS158 54LS258	4010A 4025A 4077B ^R
54LS11 54LS85 54LS160 54LS259	4011A 4027A 4081B ^R
54LS12 54LS86 54LS161 54LS266	4013A 4031A 4082B ^R
54LS13 54LS90 54LS162 54LS273	4014A 4043A 4085B ^R
54LS14 54LS92 54LS163 54LS279	4015A 4049A 4086B ^R
.54LS15 54LS93 54LS164 54LS283	4016A 4050A 4098B ^R
54LS20 54LS95 54LS165 54LS290 '	4018A 4066A 4099B ^R
54LS21 54LS96 54LS166 54LS293	4502B ^R
54LS22 54LS107 54LS174 54LS295	(Refer to page 10-5)
54LS26 54LS109 54LS175 54LS298	
54LS27 54LS112 54LS190 54LS348	4001B 4012B ^R 4030B ^{R, H}
54LS28	4017B ^R 4020B ^R
54LS30 54LS114 54LS192 54LS366	
54LS32	Memory
54LS37	Prom, Schottky TTL
54LS38	·
54LS42 54LS132 54LS197 54LS390	82S126 82S131 82S181
54LS47 54LS138 54LS240 54LS393	82S129 82S136 82S185
	82S130 82S137 82S191
	82S2708
Advanced Low Power Schottky, TTL (Refer to page 10-4)	 Ram, Schottky TTL
Advanced Low Fower Schottky, TTE (Heler to page 10-4)	93L422 93L422A 93L425
54ALS00 54ALS08 54ALS20 54ALS133	• Ram, NMOS 93L415
54ALS02 54ALS10 54ALS27 54ALS138	MK2164
54ALS04 54ALS11 54ALS30 54ALS200A	WINZIO
54AI S244A	Microprocessor, NMOS
TTL	8080A 8086 Z80A Z8002
5401 5423 5470 54121 54163	Microprocessor, I ² L
5406 5425 5472 54145 54180A	9989
5407 5450 5477 54150	•
5416 5453 5482 54154	Hybrid
• • • • • • • • • • • • • • • • • • • •	(Refer to page 10-3)

- All parts are listed in MIL-STD-975 except as referenced otherwise. The parts must be purchased to the part numbers specified in MIL-M-38510.
- 2. When no Grade 1 microcircuit is listed, a Grade 2 microcircuit may be upgraded for use in Grade 1 applications in accordance with Appendix A. A non-standard part approval is required.
- 3. Refer to Appendix D for information on radiation effects.
- The part types marked with superscripts R and H can be procured as radiation hard parts, hardened to 1 x 10⁵ rads and 1 x 10⁶ rads, respectively.

INDEX TO PREFERRED MICROCIRCUITS 1, 2, 3, 4 (Continued)

LINEAR BIPOLAR AND BI-FeT

Operationa	l Amplifiers		Voltage F	Regulators		Voltage Comparators
HA2600	LM101A		LM109	LM120K-15		LM111
HA2101A	LM108A		LM723	LM140H-12		LM139
HA2500	LM118		LM120H-05	LM140H-15		LM710
HA2510	LM124		LM120H-12	LM140K-05		
HA2520	LM148		LM120H-15	LM140K-12		
LF155	LM741		LM120K-05	LM140K-15		
LF156	LM747A		LM120K-12			
LF156A	LM1558					
LF157A	LH2101A					
Line Drive	rs	Line Receivers	F	Precision Timers	DAC	Switches
9614	_	9615	,	555	08	155A
55113		55107		556	08A	200
		55108				201

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HYBRID MICROCIRCUITS

Hybrid microcircuits are defined as microcircuits in which the circuits elements are contained on more than one die or chip, as compared to a monolithic microcircuit where all the circuit elements are contained on a single die. A hybrid microcircuit generally contains an insulating substrate or substrates on which are deposited a conductor network and sometimes thick film resistors. Semiconductor dice and sometimes passive elements are attached to the substrate. Additional connections are made between the active and passive elements, the substrate, and the package leads using interconnection wires. Hybrid microcircuits are normally low volume non-standard parts. A non-standard part approval is required for all non-standard types. General requirements for hybrid microcircuits are presented in GSFC specification S-311-200.

MICROCIRCUITS Digital, MIL-M-38510 Advanced Low Power Schottky TTL

			Grad	de 1	Grad	le 2
Commercial Part Number ¹		Function	Part No. JANM38510	Manu- facturer	Part No. ² JANM38510	Manu- facturer
54ALS00		NAND, quad 2-input			/37001BXX	
54ALS02	-	NOR, quad 2-input	-		/37301BXX	
54ALS04		Hex Inverter			/37006BXX	
54ALS08		And, quad 2-input			/37401BXX	
54ALS10]	NAND, Triple 3-input			/37002BXX	
54ALS11	Gates	AND, Triple 3-input			/37402BXX	
54ALS20	7	NAND, dual 4-input	Not	te 3	/37003BXX	Per QPL-38510
54ALS27		NOR, Triple 3-input			/373002BXX	
54ALS30	1	NAND, 8-input]		/37004BXX	
54ALS133		NAND, 13-input			/37005BXX	
54ALS240A	Duff	Octal, inverting buffer	-		/38301BXX	
54ALS244A	Buffers	Octal, noninverting buffer	1		/38303BXX	
54ALS138	Decoders	Single 3 to 8 line decoder	1		/37701BXX	

- 1. Use the JANM38510 part number for ordering, not the commercial part number.
- The "XX's" are for choice of case outline and lead finish respectively. Refer to QPL-38510 for specific choices available.
- 3. No Grade 1 version of this part is presently being supplied by any manufacturer. The Grade 2 part may be used for Grade 1 applications by upgrading in accordance with Appendix A. A non-standard part approval is required.

MICROCIRCUITS Digital, MIL-M-38510 CMOS4

Commercial			Gra	nde 1	Grade 2		
Part Number ¹		Function	Part No. JANM38510	Manufacturer	Part No. ² JANM38510	Manufacturer	
4001B		NOR, quad, 2-input			/05252BXX		
4012B ^R	Gates	NAND, dual, 4-input	Note 3	Per QPL-38510	/05052BXX	_	
4030В ^{R, Н}		Exclusive-OR Gate Quad			/05353BXX	Per QPL-38510	
4017B ^R	Counter/	Decade Counter/Divider			/05651BXX		
4020B ^R	Dividers	14-stage ripple-carry binary			/05653BXX		

- 1. Use the JANM38510 part number for ordering, not the commercial part number.
- The "XX's" are for choice of case outline and lead finish respectively. Refer to QPL-38510 for specific choices available.
- 3. No Grade 1 version of this part is presently being supplied by any manufacturer. The Grade 2 part may be used for Grade 1 applications by upgrading in accordance with Appendix A. A non-standard part approval is required.
- 4. The part types marked with superscripts R and H can be procured as radiation hard parts, hardened to 1 x 10⁵ rads and 1 x 10⁶ rads, respectively. See QPL for additional information.

Index of Preferred Thermistors

Style	Description	Specification	Refer To
311P18	Thermistor, Insulated, Negative Temp. Coeff.	GSFC S311-P-18	Page 14-2
RTH	Thermistor, Insulated, Positive Temp. Coeff.	MIL-T-23648	MIL-STD-975

THERMISTORS 1

		Tolerance	Operating	Resistance		Grade 1 and Grade 2	
Temp. Coeff.	Resistance (ohms)	at 25°C (±%)	Temperature Range (°C)	Ratio R _{25°C} /R _{MAX}	Part Number ²	Specification	Manufacturer
	2252	1	-55 to 90	10.93	311P18-01LXXX		
	2252	0.5	-55 to 70	5.71	311P18-02LXXX		Yellow
]	3000	1	-55 to 90	10.91	311P18-03LXXX		
	3000	0.5	-55 to 70	5.71	311P18-04LXXX		
Non	5000	1	-55 to 90	10.91	311P18-05LXXX	GSFC	
Neg.	5000	0.5	-55 to 70	5.71	311P18-06LXXX	S-311-P-18	Springs Instrument
	10000	1	-55 to 90	9.23	311P18-07LXXX		
	10000	0.5	-55 to 70	5.03	311P18-08LXXX		
	30000	1	-55 to 90	10.72	311P18-09LXXX	,	
	30000	0.5	-55 to 70	5.60	311P18-10LXXX		

NOTES:



 1, WARNING: Use heat sinks when soldering or welding to thermistor leads.
 2. The complete part number is 311P18 AA L DASH NUMBER LEAD STYLE: LEAD LENGTH: (01, 02, etc.) S = 32 AWG, Type C per MIL-STD-1276 Specify length in centimeters. 1R0 = 1.0, 10R = 10, 101 = 100. T = 28 AWG, Type ET per MIL-W-1687-16878 N= 32 AWG, Type N-2 per MIL-STD-1276 Minimum length is 7.6cm. E = Insulated lead (TFE), 32 AWG per MIL-I-22129; Bare lead, Style S; Tubing

(FEP), M23053/11-105c.

Index of Preferred Transformers

Style ¹	Description	Specification	Refer To
M27/103	Audio Frequency	MIL-T-27	MIL-STD-975
M27/165	Audio Frequency	MIL-T-27	MIL-STD-975
M27/166	Audio Frequency	MIL-T-27	MIL-STD-975
M27/197	Audio Frequency	MIL-T-27	MIL-STD-975
M21038/9-005	Pulse, Low Power	MIL-T-21038	MIL-STD-975

The purchase order must specify that 100% screening is required. Otherwise, when unscreened parts are purchased, they shall be subjected to screening tests, as outlined in Table 15 of Appendix C prior to use.

Index of Preferred Wire/Cable^{1, 2}

Style	Description	Specification	Refer To
M22759/9	Wire, High temperature	MIL-W-22759	Page 16-2
M22759/18	Wire, Light weight, ETFE	MIL-W-22759	Page 16-3
M22759/32/33/34/35	Wire, Lightweight, crosslinked ETFE	MIL-W-22759	Pages 16-3, 4, 5
S311P13	Wire, High voltage	GSFC S-311-P-13	Page 16-
M22759/3/11/ 12/22/23	Wire, Extruded TFE	MIL-W-22759	MIL-STD-975
M22759/16	Wire, ETFE	MIL-W-22759	MIL-STD-975
M81381	Wire, Fluorocarbon-Polyimide	MIL-W-81381	MIL-STD-975
M16878	Wire, High Temperature	MIL-W-16878	MIL-STD-975
M5086	Wire, PVC insulated	MIL-W-5086	MIL-STD-975
M17	Cable, RF, Flexible, Coaxial	MIL-C-17	MIL-STD-975
M27500	Cable, Electrical, Shielded and Unshielded	MIL-C-27500	MIL-STD-975

^{1.} GSFC WAIVES THE RESTRICTIONS AND REQUIREMENTS OF MIL-STD-975 ON THE USE OF SILVER COATED COPPER CONDUCTOR WIRE AND CABLE.

Flammability properties of these wires are controlled by the applicable specifications. However, applications in Space Transportation System (STS) payloads may require that the specific STS flammability hazards be addressed. Users are advised to consult the appropriate project systems safety officer.

WIRE Electrical, Insulated, High Temperature

Style¹	Strands No. x AWG #	Diameter over Insulation, mm		Voltage Rating, Maximum	Specification	Grade 1	Grade 2	Remarks
		Minimum	Maximum	(volts/RMS)	MIL-W-22759	Manufacturer		
M22759/9-22-X	19 × 34	1.47	1.57			QPL-22759/9		
M22759/9-20-X	19 x 32	1.68	1.78	1000	/9			
M22759/9-18-X	19 × 30	1.93	2.03	1000	/ 7			
M22759/9-16-X	19 x 29	2.11	2.21					

^{1.} For the "X" suffix, substitute the appropriate color code designator from MIL-STD-681 (listed on page 16-7).

WIRE Electrical, Insulated, Lightweight (Page 1 of 3)

Style ¹	Strands	Diameter over Insulation, mm		Voltage Rating, Maximum	Specification MIL-W-22759	Grade 1	Grade 2	Remarks	
	No. x AWG #	Minimum	Maximum	(volts/RMS)	MIL-W-22/59	Manuf	acturer		
M22759/18-26-X	19 × 38	.762	.864						
M22759/18-24-X	19 x 36	.864	.965						
M22759/18-22-X	19 x 34	1.04	1.14					Tin-coated copper conductor	
M22759/18-20-X	19 x 32	1.24	1.35					Insulated with	
M22759/18-18-X	19 × 30	1.50	1.60	600	/18	QPL-22	2759/18	extruded ETFE	
M22759/18-16-X	19 × 29	1.65	1.75					Maximum tem- perature 150°C;	
M22759/18-14-X	19 x 27	2.01	2.11					suitable for use as hookup wire.	
M22759/18-12-X	37 × 28	2.57	2.67	,					
M22759/18-10-X	37 × 26	3,15	3.25						

^{1.} For the "X" suffix, substitute the appropriate color code designator from MIL-STD-681 (listed on Page 16-7).

WIRE Electrical, Insulated, Lightweight (Page 2 of 3)

Style ¹	Strands No. x AWG#	Diameter over Insulation, MM		Voltage Rating, Maximum	Specification MIL-W-22759	Grade 1	Grade 2	Remarks
	No. x AvvG#	Minimum	Maximum	(volts, RMS)		Manufacturer		
M22759/32-30-X M22759/32-28-X M22759/32-26-X M22759/32-24-X M22759/32-22-X M22759/32-20-X M22759/32-18-X M22759/32-16-X M22759/32-14-X M22759/32-12-X	7 x 38 7 x 36 19 x 38 19 x 36 19 x 34 19 x 32 19 x 30 19 x 29 19 x 27 37 x 28	.559 .635 .762 .889 1.04 1.22 1.47 1.68 2.08 2.54	.660 .737 .864 .991 1.14 1.37 1.63 1.83 2.29 2.74	600	/32	QPL-22759		Tin-coated copper conductor, insulated with crosslined ETFE Maximum temperature 150°C
M22759/33-30-X M22759/33-28-X M22759/33-26-X M22759/33-24-X M22759/33-22-X M22759/33-20-X	7 x 38 7 x 36 19 x 38 19 x 36 19 x 34 19 x 32	.559 .635 .762 .889 1.04 1.22	.660 .737 .864 .991 1.14 1.37	600	/33	QPL-:	22759	Silver coated high strength copper alloy, Insulated with crosslinked ETFE Maximum temperature 150°C

^{1.} For the "X" suffix, substitute the appropriate color code designator from MIL-STD-681 (Listed on Page 16-7).

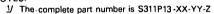
WIRE Electrical, Insulated, Lightweight (Page 3 of 3)

Style ¹	Strands No. x AWG#	Diameter over Insulation, mm		Voltage Ratings Maximum	Specification MIL-W-22759	. Grade 1	Grade 2	Remarks		
	No. x Avvg#	Minimum	Maximum	(Volts/RMS)	WIIL-W-22/59	Manufacturer				
M22759/34-24-X M22759/34-22-X M22759/34-20-X M22759/34-18-X M22759/34-16-X M22759/34-14-X M22759/34-10-X M22759/34-8-X M22759/34-6-X M22759/34-4-X M22759/34-1-X	19 x 36 19 x 34 19 x 32 19 x 30 19 x 29 19 x 27 37 x 28 37 x 26 133 x 29 133 x 27 133 x 25 665 x 30 817 x 30	1.09 1.12 1.42 1.70 1.88 2.31 2.74 3.30 4.75 5.87 7.62 9.88 10.90	1.19 1.37 1.57 1.85 2.08 2.51 2.95 3.61 5.16 6.38 8.13 10.70	600	/34	QPL-22759		QPL-22759 copper conduction insulated with crosslinked ETI		copper conductor Insulated with crosslinked ETFE. Maximum temperature
M22759/34-0-X M22759/34-00-X	1045 x 30 1330 x 36	11.91 13.39	12.73 14.30							
M22759/35-26-X M22759/35-24-X M22759/35-22-X M22759/35-20-X	19 × 38 19 × 36 19 × 34 19 × 32	.965 1.09 1.22 1.42	1.07 1.19 1.37 1.52	600	/35	QPL-22759		Silver-coated High Strength Copper Alloy Insulated with crosslinked ETFE. Maximum temperature, 200°C.		

For the "X" suffix, substitute the appropriate color code designator from MIL-STD-681 (listed on page 16-7) except that for sizes 2 and larger the braid color shall be dark green and the designator shall be 5D.

WIRE Electrical, Insulated

	600) Volt	1000) Volt	2500) Volt		Grade 1	Grade 2		
Style ¹	Strands No. x AWG #	Diameter over Insulation, mm. Max.	Strands No. x AWG #	Diameter over Insulation, mm. Max.	Strands No. x AWG #	Diameter over Insulation, mm. Max.	Specification	Manufo	acturer	Remarks	
S311P13-XX-30-Z S311P13-XX-28-Z S311P13-XX-26-Z S311P13-XX-24-Z S311P13-XX-20-Z S311P13-XX-18-Z S311P13-XX-16-Z S311P13-XX-16-Z S311P13-XX-10-Z S311P13-XX-0-Z S311P13-XX-2-Z S311P13-XX-2-Z S311P13-XX-2-Z S311P13-XX-2-Z S311P13-XX-2-Z S311P13-XX-0-Z S311P13-XX-0-Z S311P13-XX-0-Z	7 × 38 7 × 36 7 × 34 19 × 36 19 × 32 19 × 30 19 × 29 19 × 27 37 × 28 — — — —	.71 .79 .89 1.04 1.22 1.42 1.68 1.88 2.29 2.84	7 × 36 7 × 34 19 × 36 19 × 34 19 × 32 19 × 30 19 × 29 19 × 27 19 × 25 37 × 26 133 × 29 — —	 .86 1.04 1.17 1.35 1.55 1.88 2.08 2.49 3.23 3.61 5.28 		- - 1.50 1.80 2.03 2.29 2.54 3.00 3.71 4.19 5.79 7.06 8.53 10.1 12.4	GSFC S-311-P-13	Raycher	m Corp.	Tin-coated, copper conductor. Insulated with crosslinked polyalkene. Max. Temp. 135°C Suitable for use in wire harnesses.	



WIRE SIZE AWG #

COLOR CODE See page 16-7

VOLTAGE RATING 01 = 600 volts 02 = 1000 volts 03 = 2500 volts

Color Code Designators for Wire According to MIL-STD-681

Base Color	1st Stripe	2nd Stripe	Designator	Base Color	1st Stripe	2nd Stripe	Designator	Base Color	1st Stripe	2nd Stripe	Designator
Black			0	White	Black	Brown	901	White	Orange	Yellow	934
Brown			ĺ	White	Black	Red	902	White	Orange	Green	935
Red			. 2	White	Black	Orange	903	White	Orange	Blue	936
Orange			3	White	Black	Yellow	904	White	Orange	Violet	937
Yellow			4	White	Black	Green	905	White	Orange	Gray	938
Green			5	White	Black	Blue	906		3 -	,	
Blue			6	White	Black	Violet	907	White	Yellow	Green	945
Violet			7	White	Black	Gray	908	White	Yellow	Blue	946
Gray			8			,		White	Yellow	Violet	947
White			9	White	Brown	Red	912	White	Yellow	Gray	948
				White	Brown	Orange	913	1		,	1
White	Black		90	White	Brown	Yellow	914	White	Green	Blue	956
White	Brown		91 .	White	Brown	Green	915	White	Green	Violet	957
White	Red		92	White	Brown	Blue	916	White	Green	Gray	958
White	Orange		93	White	Brown	Violet	917		0.00.,	0.4)	, , ,
White	Yellow		94	White	Brown	Gray	918	White	Blue	Violet	967
White	Green		95	1	DI 01111	014)	, , ,	White	Blue	Gray	968
White	Blue		96	White	Red	Orange	923] '''''	5.00	3.07	, , , ,
White	Violet		97	White	Red	Yellow	924				
White	Gray		98	White	Red	Green	925				1
······································	Cluy		/ / /	White	Red	Blue	926	<u> </u>			
			1	White	Red	Violet	927				
				White	Red	Gray	928]			

APPENDIX A

Upgrading Grade 2 Devices for Use in Grade 1 Applications

Both PPL-17 and MIL-STD-975 have sections in which no Grade 1 part is listed. This Appendix lists what is recommended by GSFC to upgrade a Grade 2 part for use in a Grade 1 application. In most cases, GSFC guidelines are the same as those in MIL-STD-975. Where differences exist, they are defined in the appropriate paragraphs. In addition, the PPL provides upgrading alternatives to those described in MIL-STD-975 for semiconductor devices. Upgraded parts should be identified by a special marking on each piece or on the package. Where package marking is used, parts control procedures must be instituted so that the identity of upgraded parts is not lost. In all cases, the upgrading of a Grade 2 part for use in a Grade 1 application requires a non-standard part approval request.

For the upgrading of diodes, transistors, microcircuits and filters, GSFC requires the sampling plan for destructive physical analysis (DPA) to be based on a "lot". A lot is defined as all parts with identical part numbers and lot-date codes.

The sampling plan for DPA, used in this Appendix, is taken from GSFC S-311-70. The sample sizes shown below apply to all methods of upgrading of semiconductor devices given in this Appendix.

Lot Size	No. Samples
< 5	1
5-15	2
16-50	3
> 50	5

UPGRADING GUIDELINES

Section 1 — CAPACITORS

For styles listed in MIL-STD-975, see Appendix B of that document. For styles listed in PPL-17, where the appropriate Failure Rate is not available, a non-standard part approval is required to use a part with the next higher failure rate.

Section 3 — FILTERS

Grade 2 filters listed in PPL-17 and MIL-STD-975 may be upgraded for use in Grade 1 applications by performing the following additional sequence of tests and examinations:

- (a) Visually examine the filters externally, for any damage or evidence of poor workmanship in accordance with 4.6.1.1 of MIL-F-28861.
- (b) Radiographic examination in accordance with 4.6.8 of MIL-F-28861.
- (c) Thermal shock test in accordance with 4.6.2.1 of MIL-F-28861. The filters shall be mounted in accordance with 4.6.2.1b, therein. Following the test and measurements, the filters shall be maintained in their torqued and mounted configuration for the subsequent voltage conditioning tests.
- (d) Voltage condition the filters for 168 hours in accordance with 4.6.2.2.2 of MIL-F-28861. In addition to the electrical measurements required after the conditioning, visually examine the filters for any damage or evidence of physical degradation.
- (e) Hermeticity tests on hermetically sealed filters in accordance with 4.6.9b of MIL-F-28861. The fine leak rate shall not exceed 1 x 10^{-7} atm cc/sec, and there shall be no continuous stream of bubbles emanating from the filter during gross leak tests.
- (f) Destructive physical analysis in accordance with Appendix D of MIL-F-28861, except that the sample size shall be as shown on page A-1 of this Appendix.

Section 4 — FUSES

GSFC considers the fuses in Section 4 of PPL-17 to be suitable for Grade 1 use when they are screened according to Table 04 in Appendix C.

Section 5 — INDUCTORS

For styles listed in MIL-STD-975, see Appendix B of that document.

Section 6 — RELAYS

If it is not possible to use one of the S-311-P-2(06) relays listed in PPL-17, then consult the parts specialist for advice in selection of a suitable relay.

Section 7 — RESISTORS

- (a) When the appropriate Failure Rate is not available, a non-standard part approval is required to use the next higher available rate.
- (b) For resistor networks listed in MIL-STD-975, see Appendix B of that document, except that a DPA shall be performed on a sample prior to the upgrading tests. See page A-1 of this Appendix for the DPA sample size.

Section 8 — DIODES

Grade 2 JANTXV diodes listed in PPL-17 and MIL-STD-975 may be upgraded for use in Grade 1 applications by two methods:

- (a) In accordance with Appendix B of MIL-STD-975.
- (b) When a procurement consists of not more than 200 parts, perform destructive physical analysis on samples in accordance with GSFC S-311-70. Rescreen the JTXV diodes to the JANS screening requirements (except for internal visual inspection and stability tests). Power burn-in test on all parts in the lot should be extended to 360 hours with a P.D.A. of 10 percent. Measurements of electrical parameters for which delta limits are prescribed shall be made before and after the burn-in. All other electrical measurements should be made only at the completion of the burn-in with limits as specified in the detail specification.

Referring to note 1, MIL-STD-975, Page B.4, diodes can be upgraded for Grade 1 use without first rescreening them to Grade 2 level. If they have been subjected to screening verification tests (Appendix E), then tests already completed do not have to be repeated in upgrading the parts to Grade 1.

Section 9 — TRANSISTORS

Grade 2 JANTXV transistors listed in PPL-17 and MIL-STD-975 may be upgraded for use in Grade 1 applications by two methods:

- (a) In accordance with Appendix B of MIL-STD-975.
- (b) When a procurement consists of not more than 200 parts, perform destructive physical analysis on samples in accordance with GSFC S-311-70. Rescreen the JTXV transistors to the JANS screening requirements (except for internal visual inspection and stability tests). Power burn-in test on all parts in the lot should be extended to 360 hours with a P.D.A. of 10 percent. Measurements of electrical parameters for which delta limits are prescribed shall be made before and after the burn-in. All other electrical measurements should be made only at the completion of the burn-in with limits as specified in the detail specification.

Referring to note 1, MIL-STD-975, Page B.4, transistors can be upgraded for Grade 1 use without first rescreening them to Grade 2 level. If they have been subjected to screening verification tests (Appendix E), then tests already completed do not have to be repeated in upgrading the parts to Grade 1.

Section 10 — MICROCIRCUITS

Grade 2 microcircuits may be upgraded for use in Grade 1 applications by two methods:

- (a) In accordance with Appendix B of MIL-STD-975.
- (b) When a procurement consists of not more than 200 parts, the upgrading requirements given in Table 3.2 of Appendix B of MIL-STD-975 shall be used except that the DPA, therein, shall be replaced with a DPA in accordance with GSFC S-311-70 and the Group B tests of Appendix B eliminated.

Section 14 — THERMISTORS

For styles listed in MIL-STD-975, consult the parts specialist.

Section 15 — TRANSFORMERS

For styles listed in MIL-STD-975, see Appendix B of that document.

Section 16 — WIRE and CABLE

For styles listed in MIL-STD-975, consult the parts specialist.

MISCELLANEOUS

For device types listed in MIL-STD-975 but not in PPL-17, consult the parts specialist.

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APPENDIX B Parts Derating Factors

This Appendix tabulates GSFC's guidelines for the derating of the parts and device types listed in MIL-STD-975 and PPL-17. Many of these derating guidelines are identical to those given in MIL-STD-975. However, where differences occur, the GSFC derating factors shall have precedence. If a derating factor is not provided here for a specific part type, consult the Parts Specialist.

Table 01.

Derating Outline for Capacitors

Dielectric Class	Maximum Ambient Operating	Derate to Following Percentage (%)					
Dielectric Glass	Temperature °C	Rated Voltage	Ripple Voltage				
Ceramic (CKR), (CDR), (CCR)		60					
Plastic Film (CRH) (Note 1)	85	60	N/A				
Glass or Porcelain (CYR)		50					
Tantalum (Solid Electrolyte) (CSR)							
> 1 ohm/volt effective circuit impedance (Note 2)	70	60	75				
Tantalum (Wet Electrolyte) (CLR)		60					
Tantalum Foil (CLR)	70	50					

- 1. CRH styles are not approved for use in circuits where the energy is less than 250 μ joultes.
- For applications where the effective circuit resistance is less than one ohm per volt, contact the Parts Specialist.

Table 02. Derating Outline for Connectors

			М	laximum Curr		, , , ,			
Number of Contacts Used in Connector	Contact Size			W	Maximum Operating Voltage				
		16	18	20	22	24	26	28	
1 to 4	16	13.0	9.2	6.5					
1 to 4	20		į	6.0	4.5	3.3			
1 to 4	22				4.5	3.3	2.5	1.8	
5 to 14	16	9.0	7.0	5.0					050/ 5
5 to 14	20			5.0	3.5	2.7			25% of rated Dielectric Withstanding Voltage
5 to 14	22				3.5	2.7	1.9	1.4	•
15 or more	16	6.5	5.0	3.7					
15 or more	20			3.7	2.5	2.0			
15 or more	22				2.5	2.0	1.4	1.0	

Table 03. Derating Outline for EMI Filters

Class	Derate To	Maximum Ambient Temperature
All Filters	50% rated feed through current and 50% rated DC working voltage	85°C

^{1.} Maximum current may be carried by only 10% of the contacts at one time. At such time, other contacts should be limited to 100 mA.

Table 04. Derating Outline for Fuses

Subminiature 1, 2, 3, 4

Fuse Current Rating (Amperes)	Derate to the Following (%) of Rated Current	Remarks
15, 10, 7, 5 4, 3, 2½, 2	50%	
1½, 1	45%	
3/4	40%	
1/2 3/8 1/4 1/8	40% 35% 30% 25%	THE FLIGHT USE OF FUSES RATED ½ AMPERE AND LESS REQUIRES APPLICATION APPROVAL BY THE APPLICABLE GSFC PROJECT OFFICE.

- Derating factors are based on data from fuses mounted on printed circuit boards and conformally coated. For other type mountings, consult the parts specialist for recommendations.
- 2. Derating of fuses also allows for possible loss of internal gases in a space environment, which lowers the blow current rating and allows for a decrease of current capability with time.
- 3. Fuse current ratings are based on a measured blow current of 200% rated current for a maximum of 5 seconds to blow the fuse and a minimum ratio of 4/1 of blow to operating current. The minimum of 4/1 of blow to operating currents corresponds to the 50% derating factor. An 8/1 ratio of blow to operating currents corresponds to the 25% derating factor for the 1/8 ampere fuse. For maximum life in critical space applications, GSFC recommends an 8/1 ratio.

Table 05. Derating Outline for Inductors/Coils¹

Class Per MIL-C-39010	· Class Per MIL-C-15305	Maximum Operating Temperature	Derate To
-	0	65°C	
A	Α	85°C	50% of Maximum rated voltage.
В	В	105°C	

NOTES:

a) Maximum operating temperature equals ambient temperature + temperature rise + 10°C (allowance for hot spot).
 Compute temperature rise as follows:

Temperature rise (°C) =
$$\frac{R \cdot r}{r}$$
 (T + 234.5) - (T-t)

Where R = Winding resistance under load

r = No load winding resistance at ambient temperature T(°C).

- b) The insulation classes of MIL style inductive parts have maximum operating temperature ratings which are generally based upon a life expectancy of at least 10,000 hrs. The maximum operating temperatures in this table are selected to extend the life expectancy to 50,000 hrs.
- c) Custom made inductive devices shall be evaluated on a materials basis and stressed at levels below the maximum <u>rated</u> operating temperature for the materials used. Devices having a maximum <u>rated</u> operating temperature in the range of 85°C to 130°C, shall be derated to: Maximum Operating Temperature (°C) = .75 x Maximum <u>Rated</u> Operating Temperature (°C). For devices with maximum <u>rated</u> temperatures outside this temperature interval consult the parts specialist for temperature derating recommendations.

Table 06.

Derating Outline for Relays¹

Class	Derate To	Remarks
All Relays	50% of rated contact current	Users are cautioned not to derate <u>coil</u> current or voltage, as this can result in non-operation of the device.

Table 07.
Derating Outline for Resistors

Туре	Derate To	Remarks					
Carbon composition, Style RCR	60% of Rated Power						
Film, General Purpose, Style RLR	60% of Rated Power	All resistors:					
Wirewound, Accurate, Style RBR 1 % Tolerance 0.5% Tolerance 0.1% Tolerance, or less Wirewound, Power, Chassis Mount, Style RER	60% of Rated Power 35% of Rated Power 25% of Rated Power 60% of Rated Power	(a) Maximum voltage shall not exceed 80% of the maximum rated voltage on any resistor. (b) Resistors with weldable nickel leads shall be derated by an additional factor of 0.5					
Wirewound, Power, Style RWR	60% of Rated Power						
Variable Trimmers, Styles RTR & RJR	70% of Rated Current						
Film, High Stability, Style RNC	60% of Rated Power						
Film, Fixed, Networks, Style R20	60% of Rated Power						

^{1.} For additional derating guidelines, see MIL-STD-975, Appendix A.

des, Silicon	Derate to the Following Percentage							
Class	Peak Inverse Voltage	Junction Temperature						
Diodes, Silicon Rectifiers								
Diodes, Silicon Small Signal Switching	75	601, 2, 3						
Diodes, Silicon Voltage Reference, Voltage Regulator, Current Regulator, Variable Capacitor								
Diodes, Other	75 60 ^{1, 2, 3}	pe derated and recommended derating factors. Derating will						

NOTE 1: All Devices

Derate junction temperature as follows:

T₁(derated) = Derating Factor X [T₁(max)-25°C] +25°C. = Maximum allowable operating junction temperature.

T_J(max) = Manufacturer's specified maximum junction temperature.

NOTE 2: Derate average forward current (IO) to satisfy junction temperature derating calculated in note 1, as follows:

Devices Operated Without Heat Sink (Figure 1) IO(allowed) = Derating Factor X IO(max), TA ≤ 25°C

 $I_{O(allowed)} = Derating Factor \times I_{O(max)}, T_{A} = 25^{\circ}C$ $I_{O(allowed)} = Derating Factor \times I_{O(max)}, T_{A} = 25^{\circ}C$ $T_{J(derated)} = 25^{\circ}C$

IO(max) = Manufacturer's absolute maximum current rating.

TA = Ambient temperature.

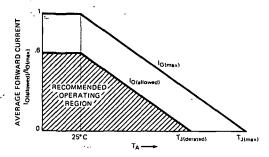


Figure 1. Derating curve from MIL-HDBK-217 modified to show operating region for devices operated without heat sinks and a Derating Factor of 0.6.

NOTES:

3. In no event shall the junction temperature exceed 125°C.

Devices Operated With Heat Sink (Figure 2)

 $\begin{aligned} & \text{IO}_{\text{(allowed)}} = \text{Derating Factor X IO}_{\text{(max)}}, & \text{T}_{\text{Case}} \leq \text{T}_{\text{D}} \\ & \text{IO}_{\text{(allowed)}} = \text{Derating Factor X IO}_{\text{(max)}} & \left[\text{I} - \frac{\text{T}_{\text{case}} - \text{T}_{\text{D}}}{\text{T}_{\text{Iderated)}} - \text{T}_{\text{D}}} \right], & \text{T}_{\text{Case}} > \text{T}_{\text{D}} \end{aligned}$

 $T_D = T_{J(derated)} - Derating Factor (T_{J(max)} - T_M)$

To = Case temperature above which Io must be further derated to satisfy derated junction temperature.

T_M = Maximum case temperature at which manufacturer permits full rated current. (I_{Omax});

I_{O(max)} = Manufacturer's absolute maximum average forward current.

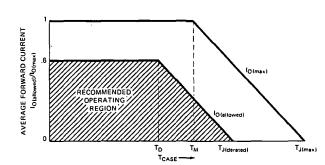


Figure 2. Derating curve from MIL-HDBK-217 modified to show operating region for devices operated with heat sinks and a Derating Factor of 0.6.

Table 09. **Derating Outline for Transistors**

Class		Derate to the Following Percentage								
Class	Voltage	Current	Power	Junction Temperature						
Silicon NPN, PNP Low power, Med. power, High power, Switching, Dual, Complimentary, Chopper, Unijunction.			·							
J-FET, MOSFET, N-Channel, P-Channel, Silicon General Purpose, Med. Power, High Power, High Speed Switching	75	75	60	601. 2. 3						
RF NPN, Other	Consult project parts enging be determined on an individual	eer for identification of param dual part type basis.	eters to be derated and recom	nmended derating factors. Derating will						

NOTE 1: All devices:

Derate junction temperature as follows:

T₁(derated) = Derating Factor X (T₁(max)-25°C) +25°C = Maximum recommended operating junction temperature.

T₁max = Manufacturer's specified maximum junction temperature.

NOTE 2: Derate power dissipation to satisfy the junction temperature derating calculated in Note 1, as follows:

Devices operated without heat sink (Figure 1)

$$P_D$$
 (allowed) = Derating Factor X P_D (max), $T_A \le 25^{\circ}$ C P_D (allowed) = $\frac{T_J$ (derated) - T_A , $T_A > 25^{\circ}$ C $R_{\theta J-A}$

P_D max = Mfr's absolute maximum power rating.

 $R_{\theta,J-A} = Junction to ambient thermal resistance from mfr's data sheet (°C/watt).$

 T_A = Ambient temperature.

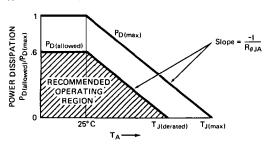


Figure 1. Derating curve from MIL-HDBK-217 modified to show operating region for devices operated without heat sinks and a Derating Factor of 0.6.

NOTE 3: In no event shall the junction temperature exceed 125°C.

Devices operated with heat sink (Figure 2)

$$\begin{split} &P_{D}\left(\text{allowed}\right) = \text{Derating Factor X} P_{D}\left(\text{max}\right), T_{\text{case}} \leqslant T_{D} \\ &P_{D}\left(\text{allowed}\right) = \frac{T_{J}\left(\text{derated}\right) - T_{\text{case}}}{R_{\theta J C}}, T_{\text{case}} > T_{D} \end{split}$$

 $T_D = T_J(derated) - R_{\theta J-C}(Derating Factor X P_D max).$

T_D = Case temperature above which power must be further reduced to satisfy junction temperature requirements.

P_Dmax = Mfr's specified absolute maximum power rating.

 $R_{\theta JC}$ = Junction to case thermal resistance specified in mfr's data sheet (°C/watt).

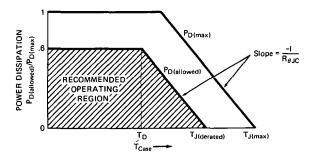


Figure 2. Derating curve from MIL-HDBK-217 modified to show operating region for devices operated with heat sinks and a Derating Factor of 0.6.

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Table 10
Derating, Outline for Microcircuits¹

Microcircuits	Dig	jital	Interface Linear					Processors, Peripherals and Memories							
Parameters	BIPOLAR	CMOS 4000 Series	A/D Converters	D/A Converters	Line Receivers	Line Drivers	Analog Switches	Opera'l. Anpl.	Voltage Compara tors	Voltage Req.	Analog Switches	смоѕ	TTL	NMOS	1 ² L
Supply Voltages Supply Current	±5% of nominal	90% of rated	±5% of nominal	±5% of nominal	±5% of nominal	±5% of nominal	90% of rated	80% of rated	90% of rated		90% of rated	±5% of nominal	±5% of nominal	±5% of nominal	±3% of
3. Power Dissipation ¹ (percent of rated power at case temperature)	80%	80%	80%	80%	80%	80%	80%	75%	75%	75%	75%	75%	75%	75%	75%
4. Frequency (percent of maximum rating)	90%	90%	90%	90%	90%	90%	:	90%			90%	90%	90%	90%	90%
5. Output Current (percent of rated current)	80%	80%			80%	80%	90%	80%	80%	80%	80%				
6. Input Voltage								70%		90%					

^{1.} The maximum case temperature is 85°C for all microcircuits.

Table 14. Derating Outline for Thermistors (Temperature Sensitive Resistor)

Class	Derate To
All Thermistors	50% of rated power

Table 15. Derating Outline for Transformers

Class Per MIL-T-27	Class Per MIL-T-21038	Maximum Operating Temperature ¹	Derate To
Q	Q	65°C	
R	R	85°C	50% of Maximum rated voltage.
s	S	105°C	,

NOTES

22000 3

1. a) Maximum operating temperature equals ambient temperature + temperature rise + 10°C (allowance for hot spot).

Compute temperature rise as follows:

Temperature rise (°C) = $\frac{R \cdot r}{r}$ (T + 234.5) · (T - t)

Where R = Winding resistance under load.

 $r = No load winding resistance at ambient temperature T(<math>^{\circ}$ C).

t = Initial ambient temperature (°C).

T = Ambient temperature at power shutoff. T shall not differ from t by more than 5°C.

- b) The insulation classes of MIL style inductive parts have maximum operating temperature ratings which are generally based upon a life expectancy of at least 10,000 hrs. The maximum operating temperatures in this table are selected to extend the life expectancy to 50,000 hrs.
- c) Custom made inductive devices shall be evaluated on a materials basis and stressed at levels below the maximum <u>rated</u> operating temperature for the materials used. Devices having a maximum <u>rated</u> operating temperature in the range of 85°C to 130°C, shall be derated to: Maximum Operating Temperature (°C) = .75 × Maximum <u>Rated</u> Operating Temperature (°C). For devices with maximum <u>rated</u> temperatures outside this temperature interval consult the parts specialist for temperature derating recommedations.

Table 16.
Derating Outline for Wire and Cable

Wire Size	Derate To – Amperes Maximum		Remarks
	Bundle or Cable	Single	Kemurks
30	0.7	1.3	
28	1.0	1.8	 Current ratings for bundles or cables are based on bundles of 15 or more wires at 70°C in a hard vacuum. For smaller bundles the allowable current may be proportionally increased as the bundle approaches a single wire. Deratings listed are for Teflon insulated wire (TYPE TFE) rated for 200°C. For 150°C wire, use 80% of value shown in table. For 135°C wire, use 70% of value shown in table. For 105°C wire, use 50% of value shown in table.
26	1.4	2.5	
24	2.0	3.3	
22	2.5	4,5	
20	3.7	6.5	
18	5.0	9.2	
16	6.5	13.0	
14	8.5	19.0	
12	11.5	25.0	
10	16.5	33.0	
8	23.0	44.0	
6	30.0	60.0	
4	40.0	81.0	
2	50.0	108.0	
0	75.0	147.0	
00	87.5	169.0	

APPENDIX C Screening of Non-standard Parts

This PPL is intended to serve as a selection source for standard parts that are properly processed and screened for use in high-reliability space flight applications. Where non-standard parts are selected for use, proper processing and screening of those parts must be determined and applied. Such determinations are the responsibility of the user, with parts engineering assistance, and must consider the type of part, its function, its design, construction, and manufacturing, as well as its significant failure modes and sensitivities. Such a screening program must be developed with the knowledge of the part's response to use, and to qualification and evaluation testing exercises.

This appendix tabulates a series of recommended screening tests for various types of parts. It is not the intent to delineate an exacting or all-inclusive set of detailed test procedures and requirements for each of a myriad of possible non-standard part selections. Rather, it is intended to stimulate the design of a detailed screening regimen to be incorporated in the part procurement document or screening specification. It brings to bear the combined experiences and knowledge of GSFC and GSFC contractor parts engineers to act as a guide in developing screening for specific parts. It is not intended to be a "cookbook" to be applied without careful consideration of the part to be screened. Furthermore, since there is generally a smaller data base for non-standard parts than preferred parts, the user must assure himself that the specified screens are non-destructive, appropriate parameters and limits are prescribed, and a lot "Percent Defective Allowable" (PDA) is included.

Other techniques, such as Destructive Physical Analysis (DPA), Residual Gas Analysis (RGA), Lot Acceptance Inspections, etc. should be applied where appropriate.

Table 01.
Screening Outline for Capacitors

Test Sequence	12, 3	2	38	4	5	6 ⁹	Reference
Category	Initial Examinations and Electrical Tests	Thermal Shock MIL-STD-202, Method 107	Seal Leak Tests MIL–STD–202, Method 112	Radiographic MIL-STD-202, Method 209	Conditioning	Final Examination	Documents MIL-STD-202
(a) Air or Glass, Variable	Visual & C, Q, DWV IR, Driving Torque	Test Condition A, except step 3		N/A	N/A		MIL-C-14409
(b) Ceramic		shall be @ max. rated temperature	N/A	In accordance with MIL-C-39014. N/A to chip styles	2 x rated voltage @ max. rated tempera- ture for 96 hours (Note 4)	:	MIL-C-20 MIL-C-39014 MIL-C-55681
(c) Glass & Porcelain		Test Condition B, except step 1 shall be @ - 55°C			2 × rated voltage @ 125°C for 96 hours		MIL-C-23269
(d) Mica	Visual & C, DF, DWV, IR	Test Condition A, except step 3 shall be @ max. rated temperature		N/A	2 x rated voltage @ max rated tempera- ture for 96 hours	Repeat	MIL-C-39001
(e) Paper and/or plastic film			Test Condition D		1.4 × rated voltage@ rated temperature for 96 hours	Initial Examination and	MIL-C-19978
(f) Polycarbonate, metallized film		Test Condition A, except step 3 shall be @ 100°C.	Test Conditions C, D	In accordance with MIL-C-19978	1.4 x rated voltage @ 100°C for 96 hours	Electrical Measurements	MIL-C-83421
(g) Tantalum Electrolytic, Wet Slug			Acid Indicator test per GSFC SP 01.23		Rated voltage @ 85°C for 168 hours		MIL-C-39006
Foil	Visual & C, DF DC Leakage	Test Condition A	Test Conditions D	N/A	Rated voltage @ 85°C (Note 5)		GSFC SP 01.23
(h) Tantalum Electrolytic, Solid (1)Hermetically sealed			Test Condition D	In accordance with MIL-C-39003	Rated Voltage @ 85°C and Surge Current Test		MIL-C-39003
(2)Non-hermetically sealed			N/A	N/A	Per MIL-C-39003/6 (Note 6)		GSFC S-311-P-17(01)
(i) High Voltage Ceramic (Note 7)	Visual & C, DF, IR, DWV, Corona	Test Condition A	N/A		Rated Voltage rated Temperature 100 hours	1	GSFC S-311-P-15(01)

NOTES: 1. Test procedures and requirements in accordance with those in the applicable Military or NASA referenced document. For additional information see the referenced document.

2. Legend: C = Capacitance, DF = Dissipation Factor, DWV = Dielectric Withstanding Voltage, IR = Insulation Resistance, Q = Quality Factor (initial electrical tests are optional).

3. Insulation resistance measurements are normally performed at +25°C, but the option is made to perform this measurement at maximum rated temperature as well.

5. Voltage conditioning shall be conducted for 168 hours for polarized styles. For non-polarized styles, voltage conditioning shall be conducted for 192 hours with the voltage polarity reversed after 96 hours.

6. Surge current testing shall be performed on CSR style capacitors for all Grade 1 applications. Where the effective series resistance is < one ohm/yolt, consult Part Specialist.

7. Testing at high voltage (DWV, corona) shall be limited to the rated voltage.

8. Seal leak tests apply only to hermetically sealed, unsleeved styles; however, the acid indicator test is required on all tantalum wet slug styles.

4. Voltage conditioning shall be performed using procedures and requirements of MIL-C-39014 Rev. C or later.

9. Final IR measurements to be made at both +25°C and at max. rated temperature.

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Table 03.
Screening Outline for EMI Suppression Filters ²

Test Sequence	1	2	3	4	5	Reference Documents
Category	Initial Measurements and examination	Thermal Shock 3,4	Seal Leak Test	Voltage Conditioning(^{5,6}	Final Measurements and examination	MIL-F-28861
Filters, EMI Suppression (with Ceramic Capacitor Elements) For Both Grade 1/ Grade 2 parts	 Visual Dielectric Withstanding Voltage (DWV) Capacitance (when applicable) Dissipation Factor (DF) (when applicable) Insulation Resistance (IR) at + 25°C and optional at max. rated temperature. D.C. Resistance Radiographic examination, Grade 1 only. 	As per MIL-STD-202, Method 107 Test Condition Condition A; except that in step 3, sample units shall be tested at 125°C, or max. rated temperature.	Fine and Gross Leak tests (applicable to hermetically sealed devices only).	As per MIL-STD-202, Method 108 at test temperature + 125°C, or max. rated temperature, ±3°C. DC rated filter is 2X rated voltage for 164±4 hours. AC rated filter is 1.2X rated voltage for 164±4 hours.	Repeat initial examinations and measurements, except radiographic examinations. Final measurements to be made at both +25°C and at max. rated temperature.	MIL-STD-202

- 1. Performance of initial electrical tests is optional.
- 2. Consult the parts specialist for assistance in screening other types of filters.
- Filters shall be mounted in a thru-hole and torqued in place on a rigid metal plate to the specified value. Not applicable to solder-in types.
- At completion of or during the final cycle and before the filter is removed from the plate, measure and record insulation resistance at +125°C, or maximum rated temperature.
- 5. For voltage conditioning, use the test circuit described in MIL-F-28861, par. 4.6.2.2.2d.
- After completion of voltage conditioning and while still at +125°C, or maximum rated temperature, the insulation resistance shall be measured per MIL-F-28861, par. 4.6.13.

Table 04.
Screening Outline for Subminiature Fuses^{1, 3}

Test Sequence	1	2	3	4	_
Category	Initial Measurements ²	Thermal Shock	Final Measurements	Acceptance Criteria	Reference Documents
Fuses, Subminiature FM04	Perform visual and mechanical inspections per paragraph 3.5 of MIL-F-23419. Measure cold resistance at 10% or less of	MIL-STD-202 method 107, condition B	Repeat Initial inspection and measurements Calculate = RHOT ₂	GSFC recommends using fuses in lower half of the voltage drop range and those where RHOT1	MIL-F-23419 MIL-F-23419/4 MIL-F-23419/8
	rated current. Subject fuses to 100% rated current for not less than 5 minutes. Maintain current at this level and measure the voltage drop within the next 5 minutes. Calculate RHOT1, (voltage drop/rated current).			and ^R HOT ₂ differ by less than 3%	

- 1. Tests shall be designed to minimize the time in excess of 5 minutes that the fuses are subjected to full rated currents. These fuses should not be operated at rated currents for more than 30 minutes or parts may be degraded so that fuse life is reduced. MIL-F-23419 specifies minimum life at 110% of Rated Current to be 1.5 hours according to lot sampling tests. Rated current according to MIL-F-23419 is "the amount of current the fuse will carry indefinitely without interruption."
- 2. Initial electrical tests are optional.
- 3. For fuses rated ½ ampere and less, time at rated current should be further minimized by measuring parameters at earliest stable reading.

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Table 05.
Screening Outline for Inductors/Coils

Test	1	2	3	4	
Sequence Category	Initial Measurements	Thermal Shock	Burn-In	Final Measurements and Delta Reject Criteria	Reference Document
Coils, Fixed, Molded, RF	 Visual Inspection D.C. Resistance Insulation Resistance (IR) Dielectric Withstanding Voltage (DWV) Inductance (L) Q Self Resonant Frequency (SRF) 	MIL-STD-202 Method 107, Condition A-1, use maximum operating temperature of coil.	MIL-STD-981 Par. 30.1.2.2	Visual Inspection Repeat initial measurements. Reject $\Delta R > \pm 5\%$ $\Delta L > \pm 5\%$	MIL-C-39010 MIL-STD-981
Coils, Audio and Power	 Visual Inspection D.C. Resistance Insulation Resistance (IR) Dielectric Withstanding Voltage (DWV) Inductance (L) Q Self Resonant Frequency (SRF) 	MIL-STD-202 Method 107, Condition A-1, Use maximum operating temperature of coil.	MIL-STD-981 Par. 30.1.2.1.2	Visual Inspection. Repeat initial measurements. Reject: △R> ±5% △L> ±5%	MIL-T-27 MIL-STD-981

Table 06.
General Screening Outline for Relays^{1, 6}

Test Sequence	1	2	3	4	5	6	7	8	9	10
Category	Initial Visual Examination	Initial Seal Leak Tests	Initial Electrical Measurements ³	Sinusodial Vibration ⁴	High Temp Soak Test	Low Temp Miss Test	Room Temp Miss Test	Final Seal Leak Test	Final Electrical Measurements	Final Visual Examination
Relays – Latching and Non–Latching	a. External Visual b. Pre-Cap Visual	MIL-STD-202 Method 112 Fine leaks Test Cond. C Gross leak Test Cond. D	Coil Resistance Pull In and Drop Out	10-2000 Hz 30 g peak	8 hrs. soak at 125°C	1000 operation miss test at -65°C	5000 operation miss test at 25°C	Repeat test sequence no. 2	Repeat test sequence no . 3	External Visual

- These screening tests are to be performed per GSFC S-311-P2(06) in the sequence shown. When the screening is
 performed by the relay manufacturer, the initial external visual (1), seal leak (2) and electrical measurements (3) are
 optional. For additional information, consult the Parts Specialist.
- 2. Pre-cap visuals are applicable only to parts procured to specification, e.g. a source control drawing (SCD), which includes pre-cap visual accept/reject criteria.
- 3. The test sequence of electrical measurements is optional; also, the performance of initial electrical measurements is optional.
- 4. For relays rated at higher than 30g, consult the Parts Specialist for screening g-level.
- 5. Drop out voltage is not applicable to latching relays.
- 6. A DESTRUCTIVE PHYSICAL ANALYSIS (DPA) SHALL BE PERFORMED PER GSFC-S-311-70 IF PRE CAP VISUAL IS NOT PERFORMED.

Table 07. (Page 1 of 2) Screening Outline for Resistors

Test	1	2	3	4	5	Reference
Sequence	Initial Measurements	Thermal Shock	Conditioning	Seal Leak Test ¹	Final Measurements and Delta Reject Criteria	Document
Resistors, Fixed, Carbon Comp.	Visual Inspection Resistance	_	_	_	-	MIL-R-39008
Resistors, Fixed, Film, General Purpose	Visual Inspection Resistance	_	1.5 x rated power at room temperature for 24 hours.	_	Visual Inspection Resistance Reject: △R>±0.5%	MIL-R-39017
Resistors, Fixed, Film, High Stability	Visual Inspection Resistance	MIL-STD-202 Method 107 Cond F	Mil. equivalent styles: Style 50, 55, 60: 5 x rated power at room temperature for 1 hour. Style 65: 4 x rated power at room temperature for 1 hour. Style 70 and 75: 2.25 x rated power at room temperature for 1 hour. Style 90: 6.25 x rated power for 5 seconds at room temperature.	MIL-STD-883 Method 1014 Cond . D (For hermetically sealed units)	Visual Inspection Resistance Reject: ΔR>±0.2% Style 90: ΔR>±0.05%	MIL-R-55182
Resistors, Fixed, Wirewound, Power	Visual Inspection Resistance	_	1.0 x rated power for 1.5 hours on, 0.5 hour off for 100 hours at 25°C.	-	Visual Inspection Resistance Reject: △R>±0.01%	MIL-R-39005
Resistors, Fixed, Wirewound, Power,	Visual Inspection Resistance	-	1.0 x rated power for 1.5 hours on, 0.5 hour off for 100 hours at 25°C.	_	Visual Inspection Resistance Reject: △R>±0.2%	MIL-R-39007
Resistors, Fixed, Wirewound, Power, Chassis Mount	Visual Inspection Resistance	_	1.0 x rated "free air" power for 1.5 hours on, 0.5 hour off for 96 hours at 25°C.		Visual Inspection Resistance Reject: △R>±0.2%	MIL-R-39009

- For resistors with nontransparent envelopes, perform the dye penetrant leak test of MIL-STD-883, Method 1014, Cond. D, except substitute the following post exposure inspection procedure:
 - (a) thoroughly cleanse the resistors to remove external dye;
 - (b) at a minimum temperature of 80°C rotate the resistors about their longitudinal axes (maintain the longitudinal axes horizontal) for a minimum of 2 minutes;
 - (c) Inspect for evidence of dye leakage.

Table 07. (Page 2 of 2) Screening Outline for Resistors

Test Sequence	1	2	3	4	5	Reference
Category	Initial Measurements	Thermal Shock	Conditioning	Seal Leak Test ²	Final Measurements and Delta Reject Criteria	Document
Resistors, Variable, Wirewound, Low Power	Visual Inspection Resistance	_	1 watt power for 1.5 hours on, 0.5 hour off for 50 hours at 25°C.	_	Visual Inspection, Resistance, Peak Noise, Continuity, End Resistance, Torque Reject: $\triangle R > \pm 0.5\%$	MIL-R-39015
Resistors, Variable, Non- Wirebound, Low Power	Visual Inspection Resistance	_	1.5 x rated power for 1.5 hours on, 0.5 hour off for 50 hours at 25°C.	-	Visual Inspection, Resistance, Contact Resistance, End Resistance Torque Reject: △R > ±2% (char. C)	MIL-R-390357
		,		MIL-STD-202	\triangle R> ±1.5% (char. F) \triangle R> ±1% (char. H)	···
Resistors, Fixed Networks	Visual Inspection Resistance MIL-STD-202 Method 107 Cond. B		1.0 x rated power for 1.5 hours on, 0.5 hour off for 100 hours at 25°C.	Method 112 Cond. C (For hermetically sealed units)	Visual Inspection, Resistance, Reject: $\triangle R > \pm 0.25\%$ (char. C) $\triangle R > \pm 0.50\%$ (char. H) $\triangle R > \pm 2.0\%$ (char. M)	MIL-R-83401

Table 08. (page 1 of 4) Screening Outline for Diodes¹

Test	1	2	3	4	5	6	7	8	9
Sequence Part Category	Internal Visual (Precap) Inspection	Initial Insp. & Electrical Parameter Measurements	High Temperature Storage	Thermal Shock (Temperature Cycling)	Acceleration	PIND.2	Seal Leak Tests	Pre-Power and Reverse Bias Burn- In Electrical Measurements	Reverse Bias Burn-In (Notes 2, 6)
a. Diodes, Small Signal, Silicon								Read and record V _F and I _R .	
b. Diodes, Switching, Silicon	MIL-STD- 750 Method 2074. This test can only be performed by the manu-	1. Visual Insp. per MIL- STD-750 Method 2071. 3X min 2. Electrical	MIL-STD-750 Method 1032 Store for 48 hrs.	MIL-STD-202 Method 107 Test Condition C, except 10	MIL-STD-750 Method 2006, except test	MIL—STD—750 Method 2052 Only for Grade	MIL—STD—750 Method 1071.1. Fine Leak: Test		MIL~STD~750 Method 1038 Test Cond. 9, 72 hrs
c. Diodes, Voltage Reference, Silicon	facturer, when specified in procurement document.)	parameter measurements (Note 1.)		cycles; except the maximum temperature shall be 125°C (Note 3.)	shall be 20,000 G in Y ₁ orientation only, one time only.	1 screening.	Condition G or H. Gross Leak: Test Condition C.	Read and record BV and Z.	Same as above except 96 hrs
d. Diodes, Voltage Regulator, Silicon			(Note 2.)			(Noté 4.)	(Note 5.)	Read and record BV, I _R and Z.	Same as above except 96 hrs Iz= maximum rated value.
e. Diodes, Power Rectifier, Silicon, (Fast Recovery or Gen, Purpose)					Same as above except 5000G			Read and record V _f and I _R .	

- Performance of electrical measurements at sequence 2 is optional. Measurements at high and low temperatures are also optional at this point, however, if performed here they need not be performed in sequence 13. For Grade 1 applications, high and low temperature measurements shall be made on all parts; whereas LTPD sampling is permissible for Grade 2 applications.
- Tests shall be conducted at the maximum operating temperature. If parts have leads that are not gold-plated, they may be subject to tarnishing
 at temperatures greater than 125°C. Therefore, such parts must be tested in an inert atmosphere. After test, leads should be inspected for
 tarnishing, and refinished if necessary.
- For axial lead glass body diodes, 10 cycles of thermal shock (glass strain) in accordance with MIL-STD-750, method 1056, test condition A, over the temperature range 0° to + 100°C shall be substituted for this test.
- 4. Particle Impact Noise Detection (PIND) shall be performed only on parts with internal package cavities.
- 5. For Grade 2 applications, the seal leak tests may be performed at either test sequence 7 or 14. For Grade 1 applications, the tests must be performed in test sequence 14; in addition the test may be performed in test sequence 7, as well.
- 6. Reverse bias remains applied at the end of burn-in until TA reaches 30°C.

Table 08. (page 2 of 4) Screening Outline for Diodes

Test	10	11	12	13	14	15	16
Sequence Part Category	Post Reverse Bias Burn-In Electrical Measurements	Power Burn-In	Post Power Burn-In Electrical Measurements	Final Electrical Parameter Measurements	Seal Leak Tests	Radiography	External Visual Examination
a. Diodes, Small Signal, Silicon		MIL—STD—750 Method 1038 Test	Read and record V_F and I_R and	Measure 25°C electrical parameters except those measured in sequence 12.			
b. Diodes, Switching Silicon	Read and record V _F and I _R .	specified v_r and I_0 with $f = 60$ Hz. $T_A = 25 ^{\circ}\text{C}$	calculate deltas.	2.) Electrical parameters at maximum operating temperature extremes. (Note 1)			
c. Diodes, Voltage Reference, Silicon	Read and record BV and Z and calculate deltas.				MIL_STD-750 Method 1071.1. Fine Leak: Test Condition G or	MIL-STD-750 Method 2076 required for Grade 1, Optional for Grade 2.	MIL-STD-750 Method 2071; 3X min.
d. Diodes, Voltage Regulator, Silicon	Read and record BV, F _R and Z and calculate deltas.				H. Gross Leak: Test Condition C. (Note 5)		
e. Diodes, Power Rectifiers, Silicon (Fast Recovery or General Purpose)		Same as (a.) above except (t _c = 100°C For stud mfg) with 60 Hz waveform applied to diode. During the half cycle when the diode is fwd biased, 1 ₀ = max rated value. During reversed bias half cycle, v _r = max rated value.	Read and record V_F and I_B and calculate deltas.				

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Table 08. (page 3 of 4)
Screening Outline for Diodes¹

Test	1	2	3	4	5	6	7	8	9
Part Category	Internal Visual (Precap) Inspection	Initial Insp. & Electrical Parameter Measurements	High Temperature Storage	Thermal Shock (Temperature Cycling)	Acceleration	PIND ²	Seal Leak Tests	Pre-Power and Reverse Bias Burn- In Electrical Measurements	Reverse Bias Burn-In (Notes 2, 6)
f. Diodes, Voltage- Variable Capacitor, Silicon					MIL-STD-750 Method 2006 except test shall be 20,000g in Y ₁ orientation only, one time only.			Read and record I _B	MIL-STD-750 Method 1039 Test Cond. A. 72 hrs
Controlled Rectifiers)	MIL-STD. 750 Method 2074. This test can only be performed by manu-	1.) Visual Insp. per MIL-STD- 750 Method 2071. 3X min.	MIL-STD-750 Method 1032 Store for 48 hrs. (Note 2)	MIL-STD-202 Method 107 Test Condition C, except 10 cycles, except the maximum temperature shall be 125°C.		MIL-STD-750 Method 2052 Only for Grade 1 screening (Note 4)	MIL—STD—750, Method 1071.1. Fine Leak: Test Condition G or H. Gross Leak: Test Condition C.	Read end record IRBXM, IFBXM, VF, VGT, and IGT	Same as above except 96 hrs at T _A = 125°C with R _{OR} and V _{FBXM} at rated values. Note: Thyristors which turn on during this burn-in shall be rejected.
	facturer when specified in procure- ment document.	Electrical parameter measurements (Note 1)		(Note 3)			(Note 5)	Read and record	
i Diodes, Switching, Schottky Barrier, Silicon					·			Read and record I _R and VB	
j. Diodes, Switching, PIN								Read and record I _R and VB.	
k. Diodes, Light Emitting					Same as above except acceleration in Z ₁ direction.		MIL-STD-750 Method 1071 Fine Leak: Test Condition H. Gross Leak: Test Condition E.	Read and record V _F and P _O .	
							(Note 5)		

Table 08. (page 4 of 4) Screening Outline for Diodes

Post Reverse Bias						16
Burn-In Electrical Measurements	Power Burn-In	Post Power Burn-In Electrical Measurements	Final Electrical Parameter Measurements	Seal Leak Tests	Radiograph	External Visual Examination
Read and record $I_{\rm R}$ and calculate delta.						
Read and record I _{RBXM} , I _{FBXM} , V _{GT} and IGT and calculate deltas.						
	MIL_STD_750 Method 1038 168 hours at T _A = 25°C and P _{OV} (Peak Operating Voltage) = max- imum rated value.	Read and record I _p and calculate delta.	1.) Measure 25°C electrical parameters except those measured in sequence 12.	MIL-STD-750, Method 1071.1 Fine Leak: Test Condition G or H.	MIL-STD-750 Method 2076 required for Grade 1 screening	MIL-STD-750 Method 2071 3X min.
	Same as above except Test Condition B. 168 hours at $T_A = 25^{\circ}C$ at specified V_f and I_o with $f = 60$ Hz.	Read and record VB and I _R and calculate deltas.	2.)Electrical parameters at maximum operating temperature extremes. (Note 1)	Condition C. (Note 5)	2	
		Read and record $V_{\rm F}$ and $I_{\rm R}$ and calculate deltas.				
	Same as above except 168 hours at: TA (or TC) = 25°C IF = 80% of maximum rated continuous forward current.	Read and record V _F and P _o and calculate deltas.		MIL-STD-750 Method 1071 Fine Leak: Test Condition H. Gross Leak: Test Condition E (Note 5)		
	delta. Read and record I _{RBXM} , I _{FBXM} , V _{GT}	Read and record I_{RBXM} , I_{FBXM} , V_{GT} and IGT and calculate deltas.	MIL—STD—750 Method 1038 168 hours at T _A = 25°C and P _{OV} (Peak Operating Voltage) = max- imum rated value. Read and record I _p and calculate delta. Read and record V _B and I _R and calculate deltas. Read and record VB and I _R and calculate deltas. Read and record VB and I _R and calculate deltas. Read and record VB and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas.	Read and record I _R and calculate deltas. MIL_STD_750 Method 1038 168 hours at T _A = 25°C and P _{OV} (Peak Operating Voltage) = maximum rated value. Read and record V _F and I _R and calculate deltas. Read and record VB and I _R and calculate deltas. Read and record VB and I _R and calculate deltas. Read and record VB and I _R and calculate deltas. Read and record VB and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas.	Read and record I _R and calculate deltas. MIL_STD_750 Method 1038	Read and record I _R and calculate deltas. MIL_STD_750 Method 1038 168 hours at T _A = 25°C and P _Q V _Q (Peak Operating Voltage) = maximum reted value. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and I _R and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas. Read and record V _F and P _O and calculate deltas

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Table 09. (page 1 of 4) Screening Outline for Transistors¹

Test	1	2	3	4	5	6	7
Sequence Part Category	Internal Visual (Precap) Inspection	Initial Inspection & Electrical Parameter Measurements	High Temperature Storage	Thermal Shock (Temperature Cycling)	Acceleration	PIND ²	Seal Leak Tests
a. Transistors, Silicon, NPN, Low, Medium Power, Switching or General Purpose	MIL-STD-750 Method 2072. (This test can	Visual Inspection			MIL-STD-750 Method 2006 except that test shall be 20,000 g in Y ₁ orientation, one time only. The 1 min hold-time requirement	MIL-STD-750 Method 2052 Only for Grade	MIL-STO-750 Method 1071, Fine Leek, Test Condition G or H. Gross Leek: Test Condition C.
Low, Medium	only be performed by the manufacturer, when specified in procurement document.)	per MIL-STD-750, Method 2071. 3X min. 2. Electrical parameter measurements (Note	MIL-STD-750 Method 1032 Store for 48 hours (Note 2)	MIL-STD-202 Method 107 Test Condition C, except 10 cycles, except the maximum temperature shall be 125°C	shall not apply.	1 screening (Note 3)	Only for Grade 2 screening (Note 4)
c. Transistors, Silicon, PNP, High Power		1)		123 0			Same as above except fine leak rejection value of 5 x 10 ⁻⁷ atm
d. Transistors, Silicon, NPN, High Power					Same as above except 5,000 g.		cc/sec. Only for Grade 2 screening
e. Transistors, Field-Effect, Junction, N-Channel, Silicon							MIL-STD-750 Method 1071,1 Fine Leak: Test Condition G or H Gross Leak: Test Condition C.
f. Transistors, Field-Effect, Junction, P-Channel, Silicon					Same as above except 20,000 g.		Only for Grade 2 screening

NOTES

- Performance of electrical measurements at sequence 2 is optional. Measurements at high and low temperatures are also optional at this point; however, if performed here they need not be performed at sequence 12. For Grade 1 applications, high and low temperature measurements shall be made on all parts; whereas, LPTD sampling is permissible for Grade 2 applications.
- Tests shall be conducted at the maximum operating temperature. If parts have leads that are not gold-plated they may be subject to
 tarnishing at temperature greater than 125°C. Therefore, such parts must be tested in an inert atmosphere. After test, leads should be
 inspected for tarnishing, and refinished if necessary.
- 3. Particle Impact Noise Detection (PIND) shall be performed only on parts with internal package cavities.
- 4. For Grade 2 applications, the seal leak tests may be performed at either test sequence 7 or 14. For Grade 1 applications, the tests must be performed in test sequence 14; in addition, the test may be performed in test sequence 7.
- 5. Reverse bias remains applied at the end of burn-in until T_A reaches 30°C in test sequence 8.

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Table 09. (page 2 of 4) Screening Outline for Transistors

Test	8	9	10	11	12	13	14	15
Part Category	Reverse Bias Burn-in (Notes 2, 5)	Pre-Burn-In Electrical Measurements	Burn-in (Notes 2, 5)	Post Burn-In Tests Measurements	Final Electrical Parameter Measurements	Seal Leak Tests (Note 4)	Radiography	External Visual Examination
a. Transistors, Silicon, NPN, Low, Medium Power, Switching or General Purpose b. Transistors, Silicon, PNP, Low, Medium Power, Switching or General Purpose Purpose c. Transistors, Silicon, PNP, High Power d. Transistors, Silicon, NPN, High Power	MIL-STD-750 Method 1039 48 Hours at: VCB = 80% of VCBO IE = 0	Read and record I _{CBO} (or I _{CES} and h _{FE} .	MIL-STD-750 Method 1039 168 hrs at specified V _{CB} (or V _{CE}) and P _T (max rated power dissipation at T _A). Same as above ex- cept P _T at case temperature specified.	Read and record I _{CBO} and h _{FE} and calculate deltas.	1.) Measure 25°C electrical parameters except those measured in sequence 11. 2.) Electrical parameters at maximum operating temperature extremes. (Note 1)	MIL_STD-750 Method 1071.1. Fine Leak: Test Condition G or H Gross Leak: Test Condition C. Only for Grade 1 screening Same as above except fine leak rejection value 5 x 10 ⁻⁷ atm cc/sec. Only for Grade 1 screening	MIL—STD—750 Method 2026 Optional for Grade 2, Required for Grade 1	MIL—STD—750 Method 2072 3X min.
e. Transistors, Field-Effect, Junction, N-Channel, Silicon f. Transistors, Field-Effect, Junction, P-Channel, Silicon		Read and record lgss. lpss and IY _{fs} I.	Same as above except at specified V _{GS} and V _{DS} .	Read and record I _{GSS} , I _{DSS} and IY _{IS} L and calculate deltas.		Same as above except Fine Leak: Test Condition G or H Gross Leak: Test Con- dition C.		

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Table 09. (page 3 of 4)
Screening Outline for Transistors¹

Test	1	2	3 .	4	5	6	7
Part Category	Internal Visual (Precap) Inspection	Initial Inspection & Electrical Parameter Measurements	High Temperature Storage (Note 2)	Thermal Shock (Temperature Cycling)	Acceleration	PIND ²	Seal Leak Tests (Note 4)
g. Transistors, Silicon, Unijunction							
h. Trensistors, Silicon, Chopper	MIL-STD-750 Method 2072. This test can only be performed by the manufacturer, when specified in procurement document.)	1. Visual Inspection per MIL-STD-750 Method 2071. 3X min. 2. Electrical parameter measurements (Note 1)	MIL-STD-750 Method 1032 Store for 48 hours. (Note 2)	MIL-STD-202 Method 107 details.) Test Conditions C, except 10 cycles, except the maximum temperature shall be 125°C.	MIL-STD-750 Method 2006 except that test shall be 20,000 g in Y ₁ orientation, one time only. The 1 min. hold-time requirement shall not apply.	MIL—STD—750 Method 2052 Only for Grade 1 screening	MIL-STD-750 Method 1071.1. Fine Leak: Test Condition G or H Gross Leak: Test Condition C. Only for Grade 2 screening.
i, Phototransistor				Sign be 120 C.		(11010 3)	MIL-STD-750 Method 1071. Fine Leak: Test Condition H. Gross Leak: Test Condition C. Only for Grade 2 Screening
j. Optically Coupled Isolator							MIL-STD-750 Method 1071.1. Fine Leak: Test Condition G or H Gross Leak: Test Condition C. Only for Grade 2 Screening

Table 09. (page 4 of 4) Screening Outline for Transistors

Test	8	9	10	11	12	13	14	15
Sequence Part Category	Reverse Bias Burn-in (Notes 2, 5)	Pre Burn-In Electrical Measurements	Burn-in (Note 2)	Post-Burn-In Electrical Measurements	Final Electrical Parameter Measurements	Seal Leak Tests (Note 4)	Radiography	External Visual Examination
g: Transistors, Silicon, Unijunction		Read and record $I_{\xi \bar{\chi} 20}$, $R_{\bar{b} \bar{b} 0}$ and η .	MIL-STD-750 Method 1039 168 hrs at specified V _{B2B1} and I _{E1} . (Maximum rated power.)	Read and record IEB20 R _{BBO} and n and calculate deltas.	1.) Measure 25°C electrical parameters except those measured in sequence 11. 2.) Electrical			
h. Transistors; Silican, Chopper	MIL-STD-750 Method 1039 48 Hours at: $V_{CB} = 80\%$ of V_{CBO} I _F = 0	Read and record I _{CBO} and h _{FE} (inverted).	Same as above except at specified V _{CB} (or V _{CE}) and P _T . (Max rated power dissipation at T _A).	Read and record I_{CBO} and h_{FE} (inverted) and calculate deltas.	parameters of maximum operating temperature extremes. (Note 1)	MIL-STD-750 Method 1071.1. Fine Leak: Test Condition G or H. Gross Leak: Test Condition C.		
							MIL-STD-750 Method 2026	
	Same as above except						Required for Grade 1	MIL-STD-750 Method 2072 3X
i. Phototransistor	48 hrs at: V _{CE} = 80% of V _{CEO} E _e (Incident Radiant Energy = 0)	Rad and record	Same as above except at specified VCE. Adjust E _e (incident radiant energy) for P _T = 80% of maximum continuous device dissipation	Read and record $I^{}_D$ and $I^{}_L$ and calculate deltas.		MIL-STD-750 Method 1071 Fine Leak: Test Condition H. Gross Leak: Test Condition C.	Optional for Grade ²	min.
	,	,						
	•							
j. Optically Coupled Isolator	Same as above except Test Condition A. VCE = 80% of VCEO	Read and record Phototransistor IC (OFF) IC (ON) hFE LEO	Same as above except at max. rated V _{CE} .	Read VI $_{\rm C}$ (OFF), I $_{\rm C}$ (ON), h $_{\rm FE}$, and I $_{\rm R}$ and record and calculate deltas.		MIL-STD-750 Method 1071.1 Fine Leak: Test Condition G or H. Gross Leak: Test Condition C.		
		I _R	P _T = 80% of maxi- mum continous device dissipation					

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Table 10. **Screening Outline For Microcircuits**

Screening Sequence	1	2	32	4		53		6	74	85
Grade	Internal Visual (Precap) Can only be performed by mfr. Specify requirement	Initial Electrical Measurements	Stabilization Bake	Temperature Cycling		constant celeration		Particle Impact Noise Detection (PIND)	Seal	Interim Electrical Parameter Measurements
1	MIL-STD-883 Method 2010 Condition A	(Note 1)	MIL-STD-883 Method 1008 condition C	MIL-STD-883 Method 1010 condition C, except the max- imum temperature shall be 125°C.	Metho condit	ortD-883 od 2001 tion E. entation only.	Me Co Lo	L-STD-883 ethod 2020 ndition A. t acceptance per ethod 2020	MIL-STD-883 Method 1014 Fine Leak: cond. A or B. Gross Leak: Cond. C.	Measure +25°C DC & AC parameters and record parameters requiring delta calculations
2	Same as Grade 1 except condition B	Same as Grade 1	Same as Grade 1	Same as Grade 1		Same as Grade 1		Same as Grade 1	Same as Grade 1	Same as Grade 1
Screening Sequence	97	10	116, 7	125, 8		134		14		
Grade	Burn-In	Interim Electrical Parameter Measurements	Reverse Bias Burn-In		Final Electrical Measurements		ic	External Visual		
	MIL-STD-883 Method 1015	Remeasure parameters	MIL-STD 883	Measure DC, AC delta parameters						

25°C. Measure DC

max, operating temperatures.

parameters @ min. and

Same as Grade 1

MIL-STD-883

Method 2012

Not required

MIL-STD-883

Method 2009

Same as Grade 1

NOTES:

1

2

1. Performance of electrical measurements at this point is optional. However, if high and low measurements are performed here, they need not be repeated in sequence 12. High and low temperature DC parameter measurements shall be made on all parts. AC parameter measurements are only required at +25°C.

specified in step 8.

cent defective.

Calculate delta and per-

CMOS only

MIL-STD 883

Method 1015

Condition A

Same as Grade 1

- 2. If parts have leads that are not gold-plated, they may be subject to tarnishing at temperatures greater than 125°C. Therefore, such parts must be tested in an inert atmosphere. After test, leads should be inspected for tarnishing, and refinished if necessary.
- 3. For microcircuit packages having an inner seal or cavity perimeter greater than 2 inches, or a mass greater than 5 grams, refer to MIL-STD-883B, Method 5004, paragraph 3.2 for acceleration instructions.
- 4. Seal and radiographic tests may be performed in any sequence after PIND test.
- 5. The parameter measurements and delta calculations required for both grade 1 & grade 2 screening shall include those parameters and deltas (including measurements for each test condition for each parameter) specified in the MIL-M-38510 slash sheet for the selected part. If no slash sheet is available for the selected part, model the parameter and delta requirements from a slash sheet for a similar part type. If no slash sheet is available for selected or similar part types, consult the Parts Specialist for recommendations.
- 6. Screening sequence 11 not required except for CMOS parts. Also, for CMOS parts, a Static I and Static II burn-in is required per MIL-M-38510 for class S devices.
- 7. The order of the burn-ins for steps 9 and 11 is optional.

240hrs @ 125°C

cuitry

160 hrs.

(Dynamic) Specify test

cond. and burn-in cir-

Same as Grade 1 except

8. Min. and Max. operating temperature parameter measurements are optional here if performed in sequence 2.

Table 14. Screening Outline for Thermistors

Test	1	2	3	4	5	6	7
Sequence	External Visual Examination	Initial Measurements	Bake	Temperature Cycle	Burn-In	Final Measurements and Tests	External Visual Examination
(a) Thermistors, (Thermally Sensitive Registor) (Negative Temp. Coef.)	MIL-T-23648 Paragraph 4.6.1	Zero-Power Resistance at 25°C and IR	100 hrs at Maximum Specified Operating Temperature	MIL-STD-202 Method 107	Not Required	Zero-Power Resistance at 25°C	MIL-T-23648 Paragraph 4.6.1
(b) Thermistors, Fixed Silicon (Positive Temp. Coef.)	4.0.1	Zero-Power Resistance at 25°C	Not Required	Cond. B	1.5 x rated pwr. for 96 hrs at 25°C	at 25 C	4.0.1

Table 15.
Screening Outline for Transformers¹

Test	1	2	3	4	5	Reference
Sequence Category	Initial Measurements	Thermal Shock	Burn-In	Seal Leak Test	Final Measurements and Delta Reject Criteria	Documents
Transformers, Audio and Power	Visual Examination Dielectric Withstanding Voltage (DWV) Induced Voltage Insulation Resistance (IR) D.C. Resistance (DCR) of each winding Primary Inductance (L) Turns Ratio	MIL-STD-202, Method 107, Test Condition A-1. Use maximum temperature specified for transformer as maximum temperature.	MIL-STD-981 Par. 30.1.2.1.	Do not perform these tests on encapsulated units. MIL-STD-202, Method 112. Test Condition C for Fine Leak. Test Condition D for Gross Leak. Use maximum temperature specified for transformer as bath temperature.	Repeat initial examinations and measurements. Reject; △L> ±5% △DCR> ±5%	MIL-T-27 MIL-STD-202 MIL-STD-981
Transformers, Pulse, Low Power	1. Visual Examination 2. Dielectric Withstanding Voltage (DWV) 3. Induced Voltage 4. Insulation Resistance (IR) 5. DC Resistance (DCR) 6. Open Circuit Inductance (OCL) 7. Leakage Inductance 8. Turns Ratio	Not Required	MIL-T-21038 Para. 4.7.4	MIL-T-21038 Para. 4.7.7 (Gross Leak Test)	Repeat initial measurements and examinations. Reject; △DCR > ±5%	MIL-T-21038

APPENDIX D Radiation Effects

The charged particles in the natural space environment pose a radiation risk to some electronic parts, because when these particles pass through them, they can significantly degrade their performance. Ground radiation tests on different electronic part types have indicated that while parts like resistors and capacitors show no noticeable degradation, many microcircuits are very sensitive to ionizing radiation. In comparison with microcircuits, most discrete semiconductor devices — with some exceptions such as microwave and MOS transistors — show much less degradation. However, some transistors, particularly small signal types, are very susceptible to radiation induced failures when operated at low collector currents. Further, the radiation environment seen by a device differs from one application to another depending upon the orbit parameters and upon location within the spacecraft, i.e., the equivalent shielding between it and the outside environment. Therefore, while selecting electronic parts, it is necessary not only to consider the device hardness, but also the application and the projected radiation environment for the application.

In dealing with the natural space environment, designers have to be concerned with two types of radiation damage, namely total dose effect and single event phenomena. The total dose effect is due to the cumulative ionization caused by the passage of all the ionizing particles through the device and is uniform over the device. This effect causes shifts in the threshold voltages of MOS transistors and can also decrease the carrier mobility in channels resulting in increased propagation delay times. In bipolar devices, current gain and junction leakage currents are adversely affected. The extent of total dose damage depends not only on the total absorbed dose but also on the dose rate and annealing characteristic of the device.

In contrast to the total dose effect, the single event upset is a localized effect which occurs when a single heavy ion or proton of high energy causes logic upset in semiconductor devices containing memory cells. This type of error is called a "soft error" as it causes no permanent damage and the device can be reprogrammed for correct functioning. However, single heavy ions can also cause latch-up, or hard errors, in devices with technologies where four layer SCR action is possible. Once latch-up is initiated in a device, control and functionality are lost. Device destruction may also result unless current is limited or power is turned off and on again.

The available radiation test data indicates that the radiation hardness of microcircuits can be expected to vary not only with the device type and technology, but also with subtle process variations continually being made by the manufacturers, i.e., with different manufacturing lots. Also, the radiation test results are strongly dependent upon the bias conditions and other details of radiation testing, such as the dose rate and the nature of irradiating source. Furthermore, the same device type can be hard with respect to single event upsets while being soft to total dose effects or vice versa. All these factors make it very difficult to specify the hardness levels for a particular part type and/or technology. However, in recent years a data base on the relative hardness of different technologies to total dose and single event upsets has emerged from the radiation tests performed by different experimenters. Table 1 gives a comparison of the susceptibilities of different technologies to the two types of natural radiation effects discussed above.

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Table 1. Comparison of Radiation Susceptibility for Microcircuits of Different Technologies.¹

Technology	Total Dose ² Hardness Level	Relative Susce	ptibility ³ To:
Technology	Rads (Si)	Soft Error	Latch-Up
DIGITAL			
NMOS	$5x10^2 - 10^4$	High	Immune
CMOS/Bulk (unhardened)	$10^3 - 10^5$	Moderate to high	Moderate
CMOS/Bulk (hardened)	$2x10^3 - 10^6$	Low	Low
CMOS/SOS	103 - 105	Very low	Immune
TTL, Low Power TTL	105 - 107	Low to High	Low
Schottky TTL, Low Power Schottky TTL	105 - 107	Low to High	None to Low
Advanced Low Power Schottky TTL	2x104 - 106	— No Data A	Available —
I ₂ Γ	$2x10^4 - 10^6$	Moderate	None too Low
ECL	$\geq 5 \times 106$	Low	None to Low
LINEAR			
CMOS	$10^3 - 2x10^7$	— No Data A	Available —
Bipolar, BI-FeT	6x10 ³ - 10 ⁷	— No Data A	Available —

- 1. Refer to pages 10-1 and 10-2 for the technologies of different microcircuits listed in this PPL.
- 2. These figures define process averages. However, some devices may not meet these levels while others may exceed them, e.g. some Schottky TTL RAM's fail much below the lower limit listed in the Table while most other devices with this technology fall within the range shown.
- 3. The single event susceptibility "ratings" listed here are relative to each other. However, a "moderate" error rate in a specific application may be unacceptably high if the application is critical.

D-2 PPL 17 September, 1984 Table 1 provides only a qualitative guideline of radiation sensitivity of microcircuits and is derived from published radiation test data. 1, 2 This often may not be sufficient as the rapid changes which have been occuring in microcircuit technology have been accompanied by changes in the radiation hardness of the parts. In general, lot sample testing may be necessary to determine the hardness levels of a procured lot of devices prior to their usage in a particular application. However, it may be noted that a number of vendors are making efforts to qualify their parts to four standard radiation levels: namely 2.5 K, 10 K, 100 K and 1 M rads. The parts qualified to these levels are identified in MIL-specifications by the symbols, M, D, R and H respectively, inserted in place of the slash mark in part markings. For more information and guidelines, consult the radiation effects specialists listed in this PPL.

BIBLIOGRAPHY

- 1. "Microcircuit Radiation Data Bank", NTIS, Access # N83-27903.
- 2. Proceedings of IEEE Annual Conference on Nuclear and Space Radiation Effects, from 1977 through 1983, published in the December issue of <u>IEEE Transaction on Nuclear Science</u>, vol. 24 through Vol. 30.

APPENDIX E

SCREENING VERIFICATION

All JANTXV semiconductors purchased to the requirements of MIL-S-19500 shall be subjected to the following 100% screening verification tests before use as Grade 2 parts. This requirement shall not apply to JANS semiconductors. These tests may be used in lieu of the JANTXV rescreening requirements specified in MIL-STD-975. Screening verification shall also be performed on nonstandard JANTX and JANTXV parts.

		MIL-S-19500 Requirement Paragraph	MIL-STD-750 Test Method
1.	External Visual		2071
2.	PIND	4.6.4.2	2052 (Condition A or B)
3.	Fine Leak		1071 Test Condition G, H
4.	Gross Leak		1071 Test Condition, A, C, E, F
5.	Initial Electrical		as specified (25 °C only)
6.	Power Burn-In or Burn-In per slash sheet		1039 (Transistors) 1038 (Diodes)
7.	Post Burn-In Electrical		as specified (25 °C only)
8.	Delta Calculation		as specified
9.	PDA	4.6.1	

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